

MKS-7**SERVICE NOTES**
*First Edition***DISASSEMBLY**

TOP PANEL REMOVAL SCREWS: ①, ②, ③ and ④

SPECIFICATIONS**MELODY, CHORD BLOCK**

DCO	LFO MOD	± 400 cents
BENDER		± 1200 cents
VCF	CUTOFF FREQ.	5Hz to 50kHz
RESONANCE		0 to self oscillation
ENV MOD	LFO MOD	± 14 octaves
BENDER		± 3.5 octaves
ENV	KEY FOLLOW	± 3.5 octaves
ATTACK TIME		+3/-2 octaves
DECAY TIME		3ms to 3s
SUSTAIN LEVEL		3ms to 12s
RELEASE TIME		0 to 100%
LFO	RATE	3ms to 12s
DELAY TIME		0.1Hz to 30Hz
		0 to 3s

BASS BLOCK

VCF	CUTOFF FREQ.	15Hz to 40kHz
RESONANCE		0 to self oscillation
ENV MOD		+9 octaves
ENV	ATTACK TIME	3ms to 2.5s
DECAY TIME		3ms to 12s
SUSTAIN LEVEL		0, 33, 66, 100%
RELEASE TIME		3ms to 12s
BASS DETUNE		± 50 cents

MASTER TUNE

± 50 cents

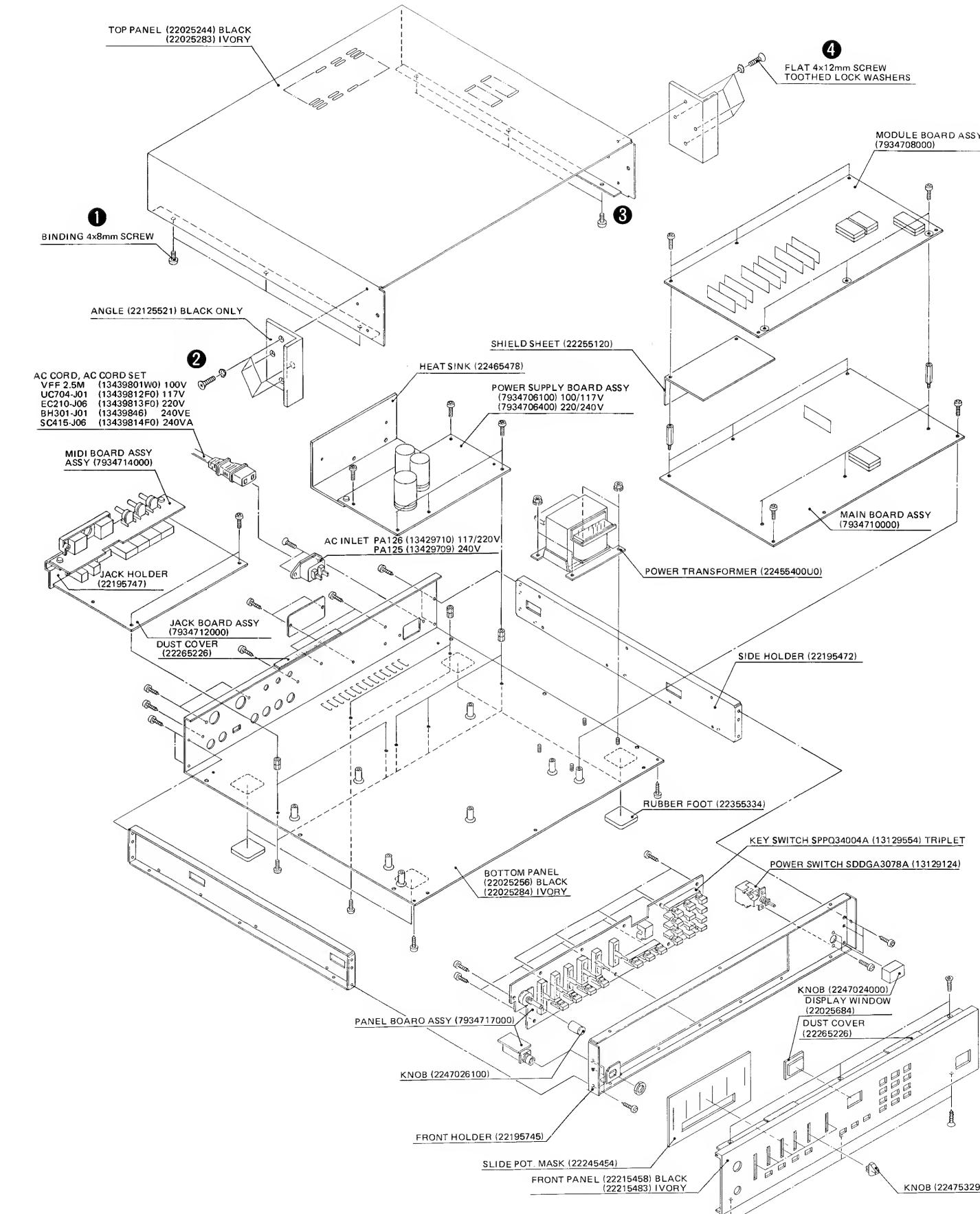
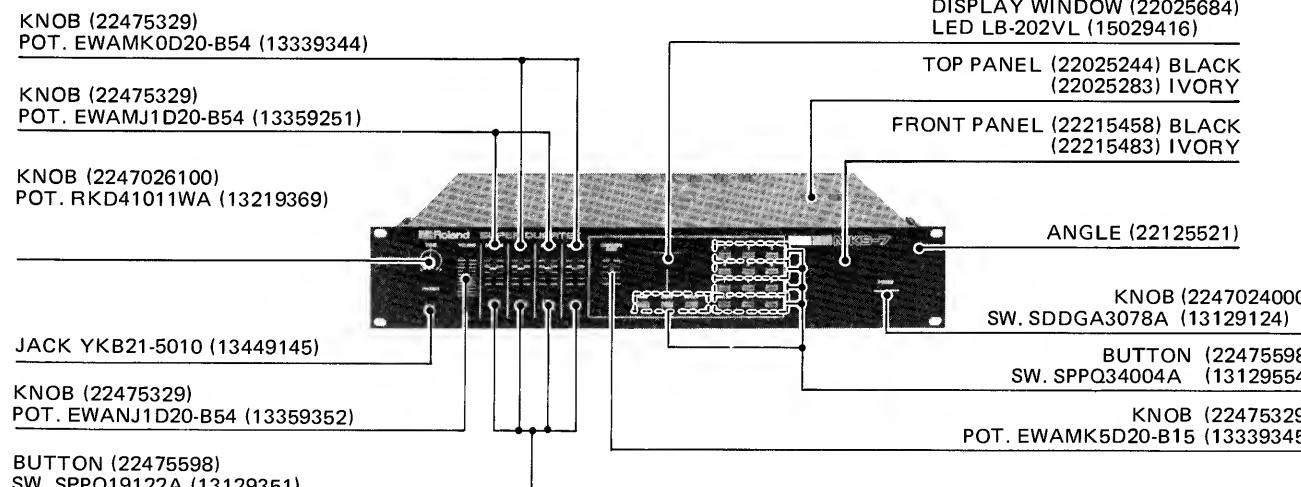
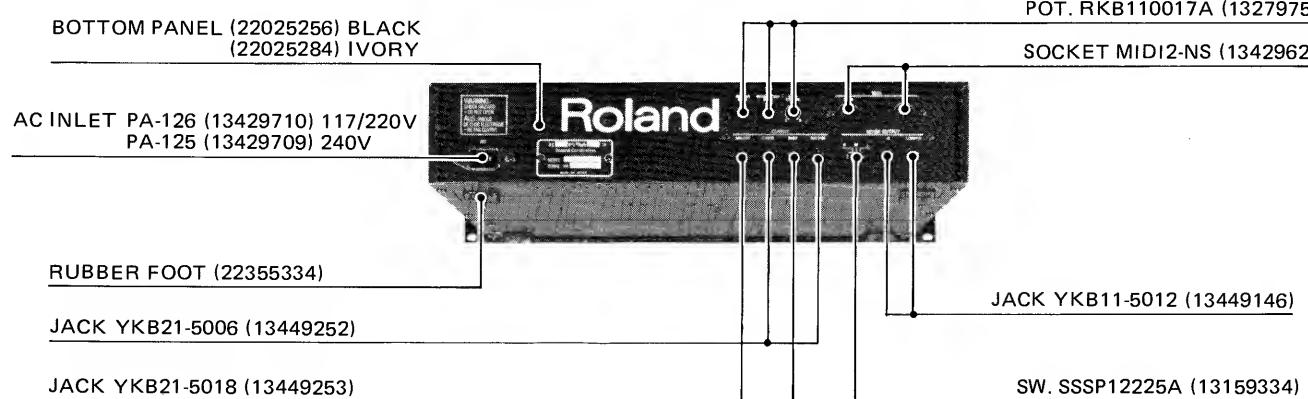
OUTPUTMIX: 1/4" phone jack 0/-15/-30 dBm
MULTI: -10 dBm**POWER CONSUMPTION**

25W

DIMENSIONSBLACK: 482(W) x 400(D) x 88(H) mm / 18-15/16" x 15-3/4" x 3-7/16"
IVORY: 430(W) x 400(D) x 88(H) mm / 16-15/16" x 15-3/4" x 3-7/16"**WEIGHT**BLACK: 7kg/15 lb 7 oz
IVORY: 7.5kg/16 lb 9 oz**ACCESSORIES**MIDI Cable (1m)
Connection Cord (2.5m) x 2**OPTIONS**Carrying Case TB-2U
MIDI/SYNC Cable

MSC-25 (2.5m)

MSC-50 (5m)

**FRONT SIDE****REAR SIDE**

PARTS LIST

PANEL		
22215458	Front	black
22215483	Front	ivory
22025244	Top	black
22025283	Top	ivory
22025256	Bottom	black
22025284	Bottom	ivory
HOLDER		
22125521	Angle	
22195745	Front	
22195472	Side	
22195747	Jack	
22195744	Cord	(100V only) black
22195788	Cord	(100V only) ivory
COVER		
22025684	Display window	
22245454	Slide pot. mask	
22265226	Dust cover	
KNOB, BUTTON		
2247024000	Knob	Power switch
2247026100	Knob	Rotary
22475329	Knob	Slider
22475598	Button	Key switch
SWITCH		
13129124	SDDGA3078A	Power switch
13129554	SPPQ34004A	Key switch triplet
13129351	SPPQ19122A	Key switch
13159334	SSSP12225A	Slide switch
PCB ASS'Y		
7934708000	Module board	(PCB 22925118)
7934710000	Main board	(PCB 22925135)
7934712000	Jack board	(PCB 22925135)
7934714000	MIDI board	(PCB 22925135)
7934717000	Panel board	(PCB 22925136)
7934706100	Power supply board	100/117V (PCB 22925137)
7934706400	Power supply board	220/240V (PCB 22925137)
JACK		
13449145	YKB21-5010	PHONES
13449252	YKB21-5006	CHORD, RHYTHM
13449253	YKB21-5018	BASS, MELODY
13449146	YKB11-5012	MIX
SOCKET		
13429710	PA-126	AC inlet (117/220V)
13429709	PA-125	AC inlet (240V)
13429628	MIDI2-NS	5P dual DIN
13429523	SMO-28-S6T	28p in IC socket
CONNECTOR		
13439260	5267-03A	3P
13439261	5267-04A	4P
13439262	5267-05A	5P
13439263	5267-06A	6P
13439264	5267-07A	7P
13439265	5267-08A	8P
13439269	5267-09A	9P
13439266	5267-10A	10P
13439206	3022-6A	6P VCF,VCA test point

I3429169	3024-05CH	5P	7-segment LED
I3439288	3021-02	2P	LED holder
FUSE			
I2559335	T-GGS 1.0A	(prim.100,117V)	
I2559336	T-GGS 2.0A	(sec.100,117V)	
I2559509	CEE-T315mA	(prim.220,240V)	
I2559513	CEE-T1A	(sec.220,240V)	
RESISTOR ARRAY			
I3910114	RGSD 4×223K	22kx4	
I3919311	RM 8-223J	11kx8	
I3919310	RM 8-103J	10kx8	
I3919146	RKM14L503F	R/2R 12bit	
I3919133	RM0621	R/2R/4R/8R/16R/32R/ 6bit	
POTENTIOMETER			
[SLIDE]			
I3359352	EWANJ1D20-B54	50kBx2	VOLUME
I3359251	EWAMJ1D20-B54	50kBx2	RHYTHM,CHORD
I3339344	EWAMK0D20-B54	50kB	BASS,MELODY
I3339345	EWAMK5D20-B15	100kB	DYNAMICS SENS.
[ROTARY]			
I3219369	RKD4101IWA	100kB	TUNE
I3279754	RKB110017A	100kB	BASS DETUNE/MODULATION SENS./BENDER SENS.
[TRIMMER]			
I3299189	RHE0AS30SA	4.7kB	
I3299177	RHE0A140XA	10kB	
I3299188	RHE0AJ40VA	22kB	
I3299190	RHE0AS40TA	47kB	
I3299178	RHE0A150RA	100kB	
FILTER			
I3529105	DSS310-55D223S	EMI filter	
POWER TRANSFORMER			
22455400U0	universal	100/117/220/240V	
DIODE			
I5019103	IS2473		
I5019125	ISS133		
I5019208	ISR35-200		
I5019245SN	SIVB10	100V 1A rectifier	
I5019254	2B4B4I	100V 2A rectifier	
I5029152	GL-9HD12	LED	
I5029416	LB-202VL	7-segment LED	
PHOTO COUPLER			
I5229706	TLP552		
IC			
I5179184	μPD7810G	CPU	
or			
I5179194	μPD7811G-101	CPU	
or			

15179190	μ PD7811G-102	CPU
15229825	MB63H114PF	8-ch counter
15179701	MBM2764-25Z	EP-ROM (Module board)
15179700D0	MBM2764-25Z	EP-ROM (Main board Except US)
15179700E0	MBM2764-25Z	EP-ROM (Main board US only)
15179633	HN61256PC42	ROM
15179635	HN61256PC44	ROM } PCM
15179661	HN61256PC71	ROM } Rhythm
15179662	HN61256PC72	ROM }
15179185 or	M82C53-5	Triple programmable interval timers
15179185N0	μ PD71054C	Triple programmable interval timers
15219147	μ PC624C	8-bit D/A converter
15159503	TC40H000P	Quad 2-input NAND gate
15159505	TC40H004P	Hex inverter
15159514	TC40H032P	Quad 2-input OR gate
15159525	TC40H139P	Dual 2-to-4-line decoder/demultiplexer
15159535	TC40H151P	8-to-1-line data selector/multiplexer
15159532	TC40H161P	Synchronous presettable 4-bit counter
15159507	TC40H273P	Octal D-type flip-flop
15159508	TC40H373P	Octal D-type latch
15159105H0	HD14013BP	Dual D-type flip-flop
15159141T0	TC4040BP	I2-stage binary counter
15159128T0	TC4050BP	Hex buffer/converter non-inverting type
15159113H0	HD14051BP	Single 8-ch multiplexer/demultiplexer
15159114H0	HD14052BP	Differential 4-ch multiplexer/demultiplexer
15159129H0	HD14053BP	Triple 2-ch multiplexer/demultiplexer
15159116T0	TC4069UBP	Hex inverter
15159133H0	HD14174BP	Hex D-type flip-flop
15159301H0	HD14520BP	Dual binary up counter
15169117H0	HD7407P	Hex buffer O.C.
15229816	MC5534A	DCO
152298170A	A1QH80017A	VCF, VCA
15229807	IR3R01	Envelope generator
15229802	BA662A	VCA
15219217	MN3006	BBB
15219213	MN3009	BBB
15169504	MN3101	BBB driver
15219124	μ PCI252H2	VCA
15189119J0	NJM062	Low-power JFET-input OP Amp
15189154	TL064CN	Low-power JFET-input OP Amp
15189147	NJM072D	Low-noise JFET-input OP Amp
15189158	μ PC4082	JFET-input OP Amp
15189171	M5218P	Low-noise OP Amp
15189136	M5218L	Low-noise OP Amp
15219149	MM5437N	Noise generator
15219152	PST520D	Reset IC
15159701	M54522	Transister array
15149110	M54562	Transistor array
15199106NH	μ PC7805H	+5V voltage regulator
15199118N0	μ PC7815H	+15V voltage regulator
15199102N0	μ PC7915	-15V voltage regulator

CAPACITOR

13659214M0	ECET25R682SW	6800 μ F/25V
13659223M0	ECET35R332SW	3300 μ F/35V
13529104	DE7150F472MVAI	0.0047 μ F Line bypass(KC)

TRANSISTOR

15119106DR	2SA933-R
15129108	2SC945-P
15129113	2SC1740-R
15129136	2SC2878-A
15139118B0	2SK381-C-P

AC CORD,AC CORD SET

13439801W0	VFF 2,5M	(100V)
13439812F0	UC 704-J01	(117V)

13439813F0	EC 210-J06	(220V)
13439846	BH 301-J01	(240VE) England
13439814F0	SC 415-J06	(240VA) Australian

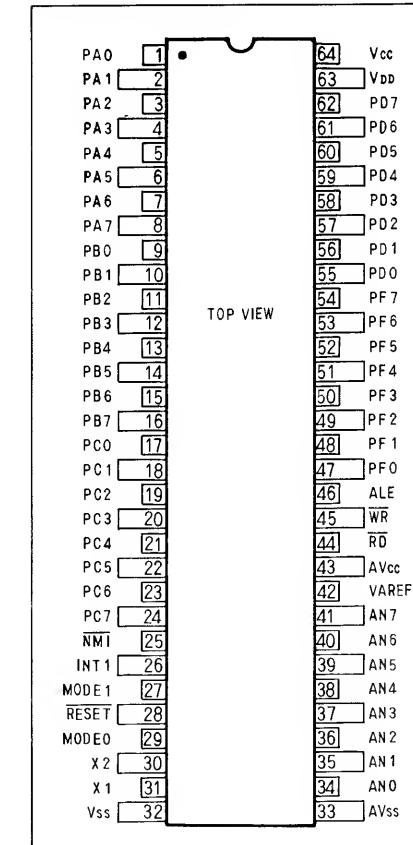
OTHERS

22355334	Rubber foot
22255120	Shield sheet
22465478	Heat sink
12389735	CSA 1.60MK
12389728	1.6MHz Xtal(ceramic resonator)
12389719	8MHz Xtal(ceramic resonator)
13719901	12MHz Xtal(ceramic resonator)
12369504	R25NQJ82ohm
	820hm Flame proof type resistor
	AC cord bushing(100V only)

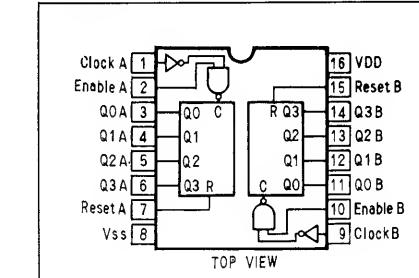
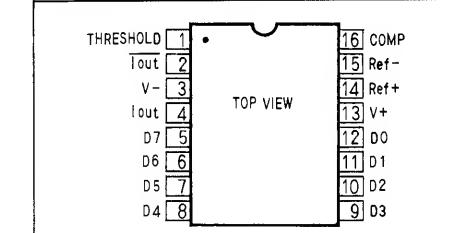
COMMERCIALLY AVAILABLE

23485167	348-167	MIDI cable (1m)
23430675	LP-25	Connection cord (2.5m)
	MSC-25	MIDI/SYNC cable (2.5m)
	MSC-50	MIDI/SYNC cable (5m)

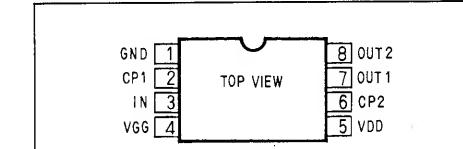
IC DATA

 μ PD7810G/ μ PD7811G

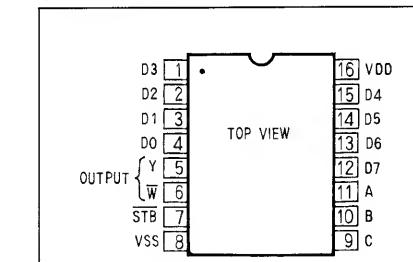
HD14520BP

 μ PC624C

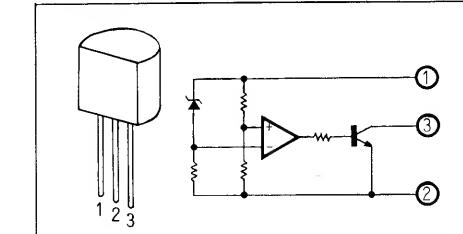
MN3006



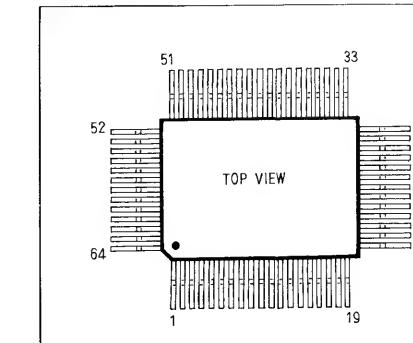
TC40H151P



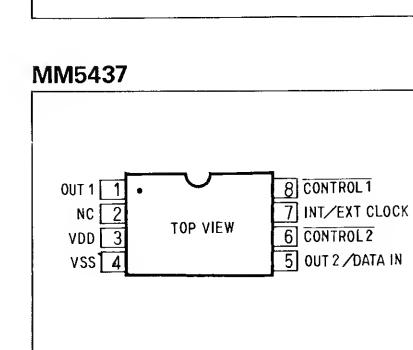
PST520D



MB63H114



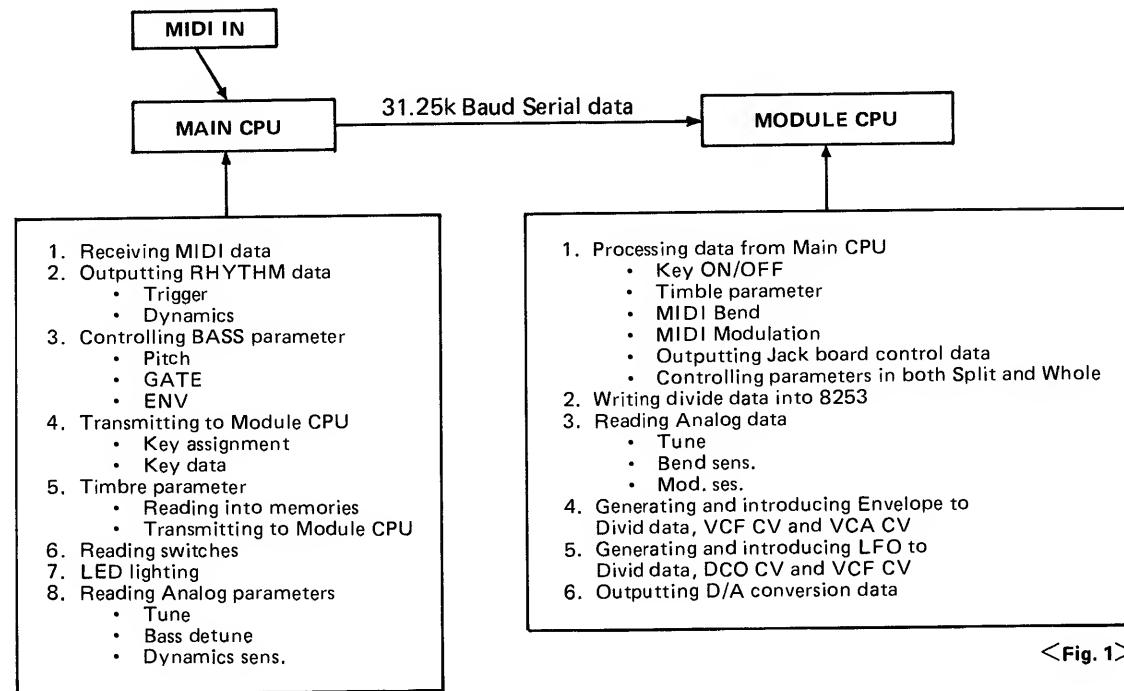
PIN	name	PIN	name	PIN	name	PIN	name
1	INHO	17	ADR2	33	XRES	49	TST2
2	ADRC	18	ADR1	34	OSCI	50	XSTA
3	A	19	ADR0	35	SC00	51	MSEL
4	D	20	CST0	36	SC01	52	CLK1
5	B	21	CST2	37	CLK0	53	CLK2
6	ADR7	22	CST4	38	XST0	54	CLK3
7	C	23	CST6	39	XST1	55	CLK4
8	ADR6	24	GATE7	40	XST2	56	XCK0
9	ADR8	25	GATE6	41	XST3	57	XCK1
10	VSS	26	VDD	42	VSS	58	VDD
11	ADR3	27	GATE5	43	XOUT	59	XCK2
12	ADR5	28	GATE4	44	XST4	60	XCK3
13	ADR8	29	GATE3	45	XST5	61	XCK4
14	ADR4	30	GATE2	46	XST6	62	XCK5
15	ADR3	31	GATE1	47	XST7	63	XCK6
16	ADRA	32	GATE0	48	TST1	64	XCK7



CIRCUIT DESCRIPTION

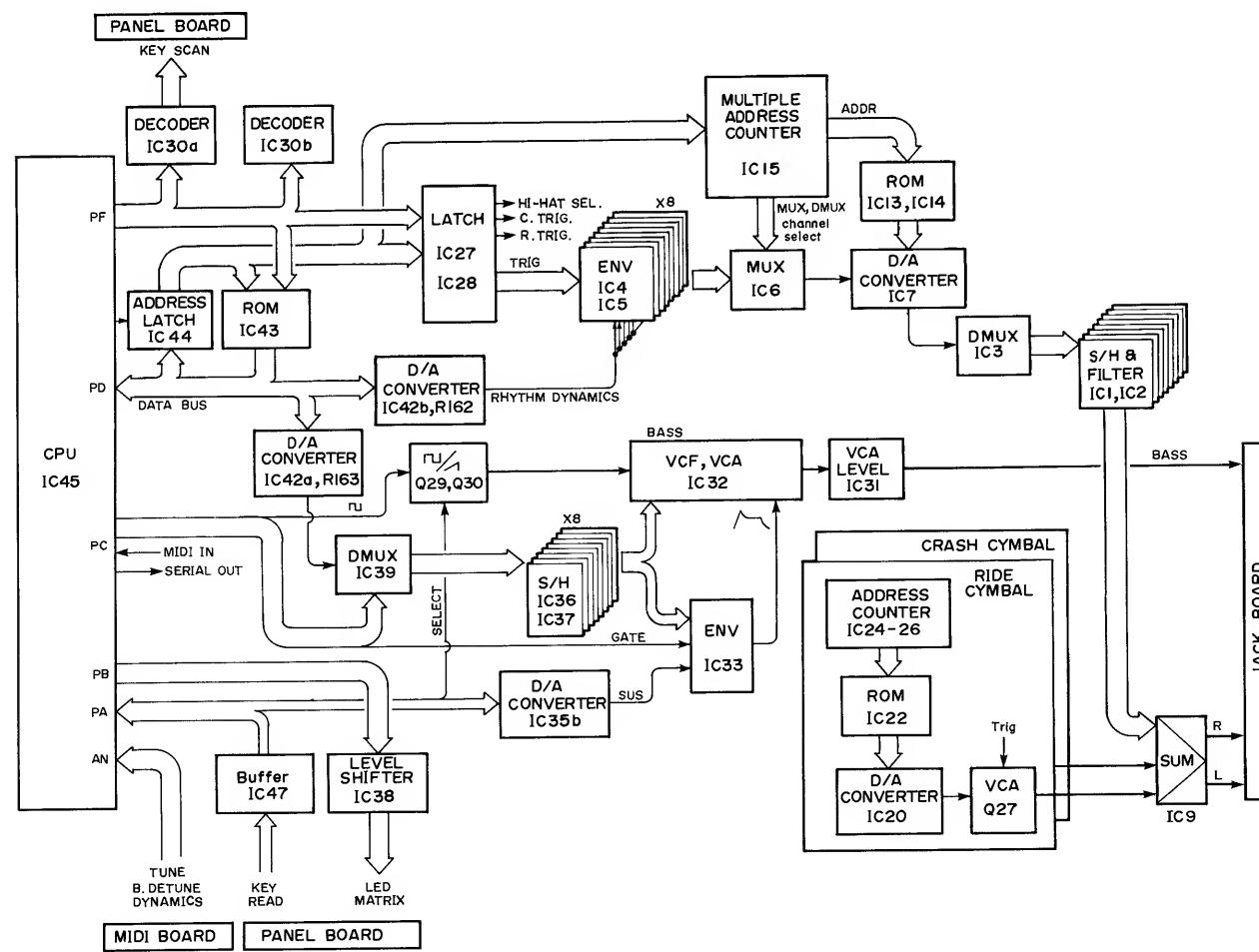
GENERAL

There are two CPUs on the MKS-7: MAIN CPU (IC45 on MAIN BOARD) and MODULE CPU (IC31 on MODULE BOARD). Two CPUs share the tasks as shown below:



<Fig. 1>

MAIN BOARD



<Fig. 2 MAIN BOARD Block Diagram>

DESIGNATION		PIN No.	FUNCTION	I/O
AN (ANALOG INPUT)	ANO 1 2 3 4 5 6 7	34 35 36 37 38 39 40 41	Master tune Bass detune Dynamic sense } NC	-
PORT A	PA0 1 2 3 4 5 6 7	1 2 3 4 5 6 7 8	} SW data read } Bass ENV sustain level select Bass waveform select	- - 0 0
PORT B	PB0 1 2 3 4 5 6 7	9 10 11 12 13 14 15 16	} Display LED drive	0 0 0 0 0 0 0 0
PORT C	PC0 1 2 3 4 5 6 7	17 18 19 20 21 22 23 24	Serial out to Module CPU MIDI serial input Bass gate Bass S/H DMUX inhibit } Bass S/H DMUX channel select Bass pitch clock	0 - 0 0 0 0 0 0
PORT D (DATA BUS)	PDO 1 2 3 4 5 6 7	55 56 57 58 59 60 61 62	} ROM address (out) } ROM data (in) } Rhythm, Bass D/A data (out)	I/O I/O I/O I/O I/O I/O I/O I/O
PORT F	PF0 1 2 3 4 5 6 7	47 48 49 50 51 52 53 54	} ROM address } Latch address ROM/Latch select } LED dynamic scan and } SW DMUX address	0 0 0 0 0 0 0 0
Xtal-1 Xtal-2 RESET RD WR ALE MODE 0 MODE 1		31 30 28 44 45 46 29 27	} Internal – clock oscillator Reset pulse ROM read pulse Latch write pulse Address latch pulse 1 } External ROM mode 0	- - - 0 0 0 - -

<Table 1a Main CPU Pin Designation (μPD7810G/7811G) >

CPU		40H139 IC30-b	ROM IC43	Bass D/A Latch	Rhythm D/A Latch	Rhythm TRIG & Dynamics Hold
PF	5	(G)	0 ↓ 0000H to 1FFFH	0	1	1
	4	(B)		0	1	1
	3	(A)		1	0	1
	2					Hi-Hat Close/open select
	1					Ride cymbal
	0					Crash cymbal
PD	7	40H373 IC44 Address Lower Latch	0000H to 1FFFH			Clap.
	6					Rim Snot
	5					Hi-Hat
	4					Hi Tom
	3					Mid Tom
	2					Low Tom
	1					Snear Dr.
	0					Bass Dr.
Available timing		ALE	RD	WR	WR	WR

Table 1b Address Map

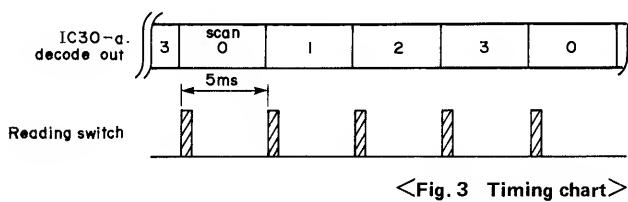
1. SWITCH READING

Main CPU IC45 reads 19 front panel switches through 4 row x 5 column Matrix on the Panel Board.

	IC30 a	Port A				
		0	1	2	3	4
Scan 0	B A 0 0	[3]	[6]	[9]	Transpose	Melody Select
Scan 1	0 1	[2]	[5]	[8]	[0]	Chord Select
Scan 2	1 0	[1]	[4]	[7]	MIDI ch	Bass Select
Scan 3	1 1	Melody	Chord	Bass	Rhythm	

<Table 2 Switch Matrix>

Switch scanning data from PF6 and PF7 of CPU IC45 are decoded at Line Decoder IC30a which pulses one of its outputs SCANO-SCAN3 (on the matrix rows) low. The switch status on the low row is read into PA0-PA4 of the CPU through Inverting Buffer IC47. The CPU repeats the scanning every 5ms for the remaining switches, cycling at 20ms intervals.



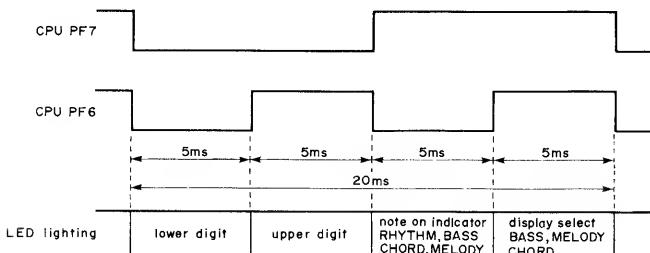
<Fig. 3 Timing chart>

2. LED LIGHTING

The LEDs are placed on the 8 x 4 Matrix as shown in Table 3. For these LEDs, switch scan data is concurrently used for lighting. It is fed to LED Driver IC3 on the Panel Board to have a low at one of IC3 outputs, providing a return path for an array of LEDs. With drive current supplied through LED Driver IC38 on the Main Board, each LED lights up dynamically at 20ms intervals on a 25% duty cycle current.

P F 7 6	Port B							
	7	6	5	4	3	2	1	0
lower digit								
0 0	(a)	(b)	(f)	(g)	(dp)	(c)	(d)	(e)
upper digit								
0 1	(a)	(b)	(f)	(g)	(dp)	(c)	(d)	(e)
note on indicator								
1 0						Rhythm	Bass	Melody
display select								
1 1						Bass	Melody	Chord

<Table 3 LED Matrix>



<Fig. 4 Timing chart>

3. POTENTIOMETER READING

CPU IC45 has a built-in A/D converter. Three of the eight Analog Inputs are used for potentiometer readings as shown in Table 4.

AN	Analogue Parameter
0	Master tune
1	Bass Detune
2	Dynamics Sens

<Table 4>

Timing for reading is determined by the CPU Main program.

4. MIDI MESSAGE READING

Photo coupler IC1 on MIDI board shifts MIDI messages to the TTL level and sends them to CPU IC45.

5. ROM READING AND SERIAL TRANSMITTING

Tone color parameters of MELODY, CHORD, and BASS blocks are stored in ROM IC43.

Upon receiving MIDI program change message, CPU IC45 first determines: Tone color parameter block (MELODY, CHORD, or BASS) being assigned to that MIDI CH.

Tone Number represented by the program change message.

Then the CPU accesses the correct memory location in ROM and transfers it (timbre parameter) to the destination in serial format as follows.

MELODY or CHORD parameter to Module CPU through SERIAL OUT.

BASS parameter to Latch IC41 through PORT D.

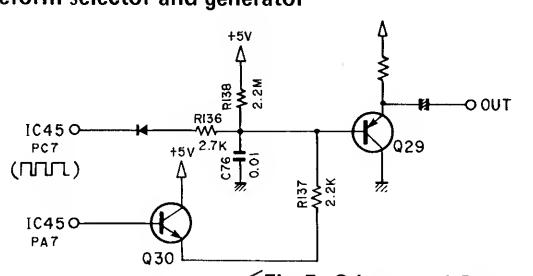
6. BASS BLOCK

a) Pitch generator

Pulse from PC7 of CPU IC45 is 1 octave lower than that of the MIDI note number. Duty factor of the pulse is 1/16, 1/8 or 1/4, depending on the tone color. When the sawtooth is selected by the waveform selector, only the pulse, of 1/16 duty factor is supplied.

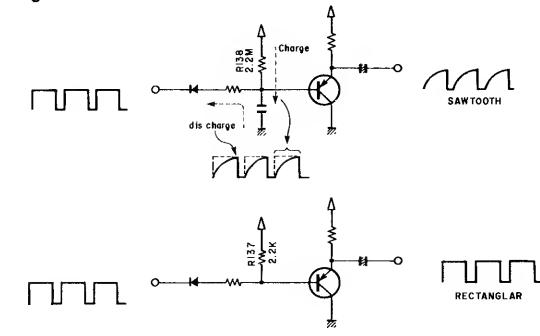
Pitch is variable ± 50 cents from MASTER TUNE and ± 50 cents from BASS DETUNE, total 100 cents detune can be accomplished.

b) Waveform selector and generator



<Fig. 5 Selector and Generator>

Output from PA7 of CPU IC45 turns on or off Q30. The resultant circuit connections and waveforms are as shown in Fig. 6.



<Fig. 6 Equivalent circuits>

c) Bass parameter sample and hold

BASS parameter data sent out from P00-PD5 of CPU IC45 is latched into Latch IC41. The 6-bit data is converted to analog form at R-2R ladder network R163 and is sampled into correct hold amp by DMUX IC39 as shown in Table 5.

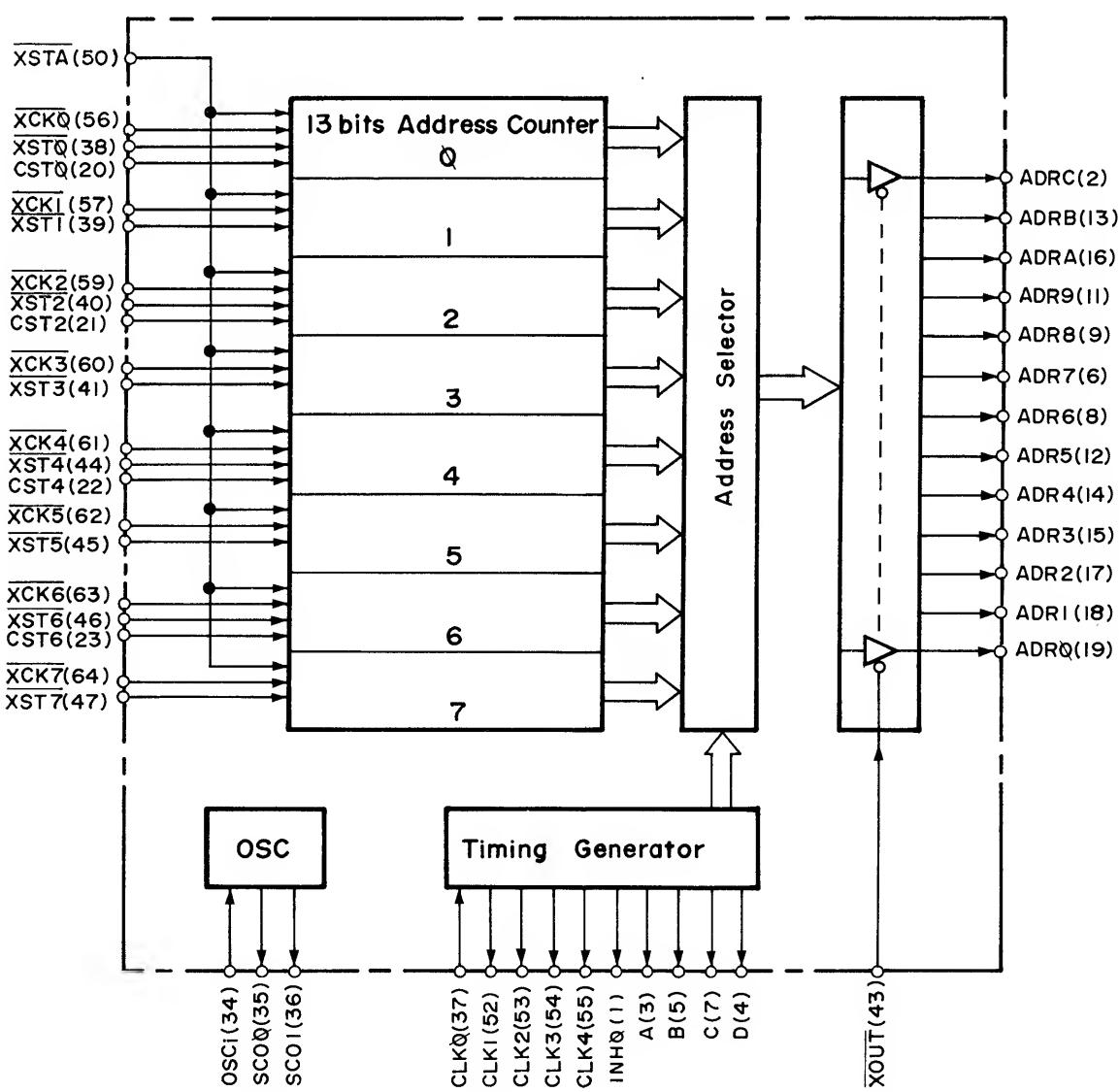
	4051 (IC39)			TL064		Parameter name
	C	B	A	IC	OUT	
CPU 4051	0	0	0	36	Pin 14	Env Attack
PC	0	0	1	37	7	Env Decay
3 inhi	0	1	0	37	1	VCA level
4 A	0	1	1	36	8	Env Release
5 B	1	0	0	37	14	VCF Resonance
6 C	1	0	1	36	7	VCF Env.modulation
D/A conversion data output:	1	1	0	37	8	VCF Key follow
IC42(a) Com	1	1	1	36	1	VCF Cutoff freq.

<Table 5 DMUX channel select>

Sustain level data from PA5 and PA6 of CPU IC45 is converted into the control voltage by 2 bit D/A converter IC35b and is supplied to Envelope generator IC33.

7. RHYTHM BLOCK

MB63H114 IC15 is a custom LSI Multiple Address Counter for use in PCM Rhythm machine, having built-in eight 13-bit address counters, a clock generator, a timing generator, and an address selector.



<Fig. 7 MB63H114 Block Diagram>

DESIGNATION	PIN	FUNCTION	I/O			
CST	0 2 4 6	20 21 22 23	+5V pull up			
XST	0 1 2 3 4 5 6 7	38 39 40 41 44 45 46 47	Counter select			
GATE	0 1 2 3 4 5 6 7	32 31 30 29 28 27 25 24	NC	0 0 0 0 0 0 0 0		
XCK	0 1 2 3 4 5 6 7	56 57 59 60 61 62 63 64	Counter clock input			
ADR	0 1 2 3 4 5 6 7 8 9 A B C	19 18 17 15 14 12 8 6 9 11 16 13 2	ROM address	0 0 0 0 0 0 0 0 0 0 0 0 0		
A B C D INH0		3 5 7 4 1	MUX, DMUX channel select	0 0 0 0 0		
CLK	1 2 3 4	52 53 54 55	DMUX inhibit	0 0 0 0		
XOUT XSTA XRES OSCI SCO0 SCO1 CLK0 MSEL TST1 TST2		43 50 33 34 35 36 37 51 48 49	NC Latch clock	ROM chip enable	MUX inhibit	0 0 0 0

<Table 6 MB63H114 Pin Designation>

Sound data in ROM IC13 or IC14 is latched into the Latch IC8 when address data^{*1} is sent to ROM from an address counter in IC15. Latched sound data is converted to an analog voltage and becomes a sound signal at D/A converter IC7, having an envelope^{*2} which is fed from MUX IC6. The sound signals, which are distributed to correct hold amp by DMUX IC3, go to the Right channel and the Left channel of Jack Board after passing the filter.

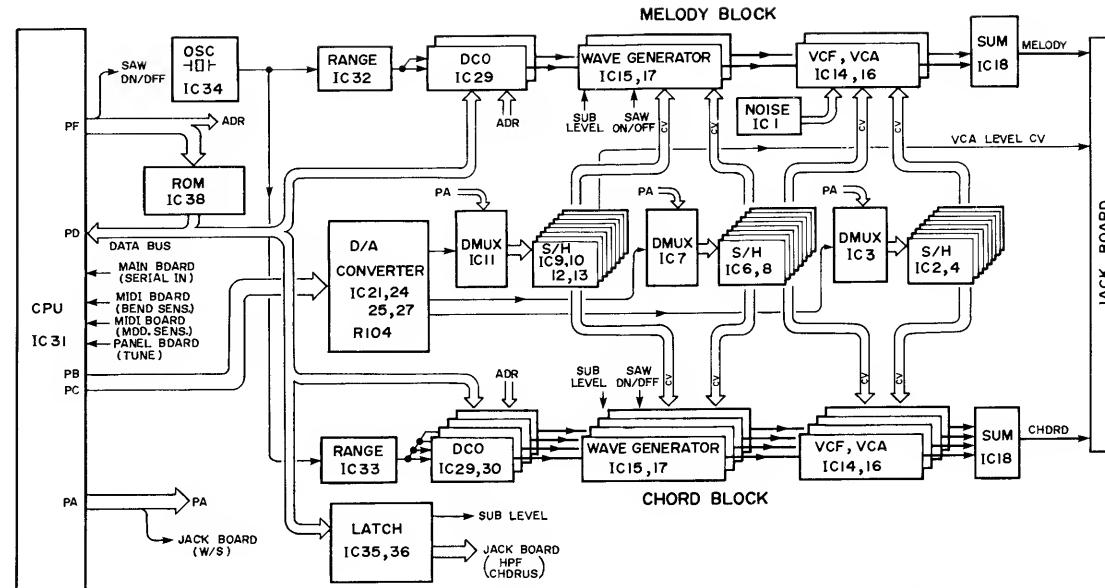
*¹ With BD, SD, RS, and CP selected LSB ADR0 is defeated at Digital DMUX IC16 and "0" is fed to ROM instead, this is because each of these sound data shares an address area with another sound data (having odd address) which is unused MKS-7.

*2 In the case of Hi-HAT, Envelope is obtained by turning transistor Q8 ON or OFF with HI-HAT select, thus controlling VCA Q9.

Crash cymbal circuit is similar to Ride cymbal circuit in configuration. Crash cymbal sound data in ROM IC21 is latched into Latch IC18 every time the address counter IC23, IC24, and IC26 increments by one step.

The latched sound data is converted into an analog voltage at R-2R ladder network R160 and becomes a sound signal having an envelope at VCA Q28.

MODULE BOARD



<Fig. 8 MODULE BOARD Block Diagram>

DESIGNATION	PIN No.	FUNCTION	I/O	
(ANALOG INPUT)	AN0	34	Tune	
	1	35		
	2	36	}	
	3	37	NC	
	4	38	Modulation sens	
	5	39	Bender sens	
	6	40	}	
	7	41	NC	
PORT A	PA0	1		
	1	2		
	2	3		
	3	4		
	4	5	S/H multiplex channel select	
	5	6	A	
	6	7	B	
	7	8	C	
PORT B	PB0	9		
	1	10		
	2	11		
	3	12		
	4	13		
	5	14		
	6	15		
	7	16		
PORT C	PC0	17		
	1	18		
	2	19		
	3	20		
	4	21		
	5	22		
	6	23		
	7	24		
PORT D (DATA BUS)	PD0	55		
	1	56		
	2	57		
	3	58		
	4	59		
	5	60		
	6	61		
	7	62		
PORT F	PF0	47		
	1	48		
	2	49		
	3	50		
	4	51		
	5	52		
	6	53		
	7	54		
Xtal-1 Xtal-2 RESET RD WR ALE MODE 0 MODE 1		31 30 28 44 45 46 29 27	Internal – clock oscillator Reset pulse ROM read pulse 8253/latch write pulse Address latch pulse 1 } External ROM mode 0 }	I I I O O O I I

Table 7 MODULE CPU Pin Designation (μ PD7810G/7811G)

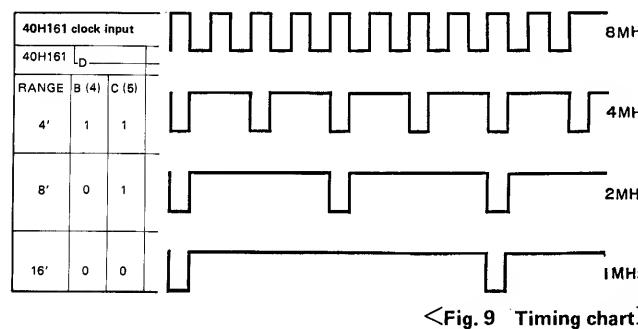
2-Voice (Melody)		4-Voice (Chord)		Description			
IC35		IC36		Range Select	16'	8'	4'
15		15			0	0	1
12		12			0	1	1
10				Sub level	0	1	2
7				Select	0	0	1
5					0	1	1
2					1	0	1
				H.P.F	1	= ON	
					0	= OFF	
				2	Chorus	1	= ON
						0	= OFF

<Table 8 Latch data>

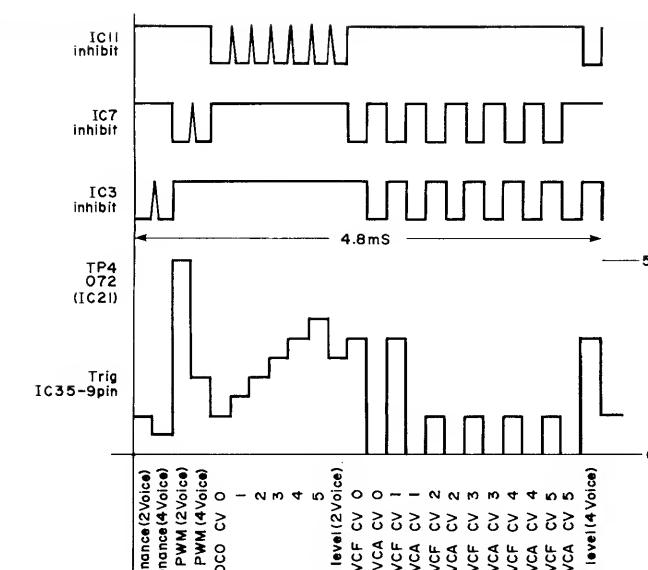
Module Board consists of MELODY BLOCK (2 Voices) and CHORD BLOCK (4 Voices).

a) OSC

The oscillator consists of a master oscillator IC34 (8MHz) and Binary counters IC32, 33. The Binary counters divide 8MHz by two, four or eight (see Fig. 11) according to RANGE (4', 8', 16') of Exclusive message and feed it to DCOs IC29, 30 which are 16-bit Programmable interval timers.



<Fig. 9 Timing chart>



<Fig. 10 D/A & S/H timing chart>

b) DCO

Each of six counters (three per timer) divides OSC frequency by a number defined by a divide data represented on the data bus of the Module CPU IC31.

The divide data is the sum of a key number and the outputs from LFO, Bender and Tune for a particular note.

The resultant at the output of each counter will be a rectangular at an audio frequency.

c) D/A Converter

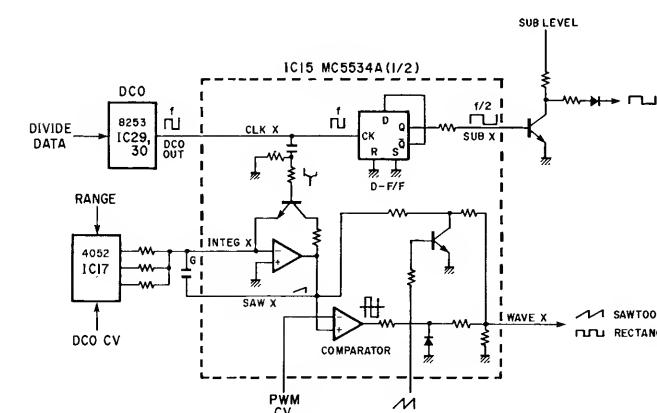
In controlling voices the Module CPU does not output each parameter independently, rather, it integrates some of parameters that are needed for a particular destination (DCO, VCF or VCA) and represents them as a 12-bit data (upper 6 bits at PB0-PB5 and lower 6 bits at PC2-PC7).

The data is converted into an analog voltage which is conditioned and routed to the destination module from the DMUX IC3, 7 or 11 as shown Fig. 12.

Ch.	Pin	IC11	IC7	IC3
7	4	DCO CV 1	VCF CV 1	VCA CV 1
6	2	2	2	2
5	5	3	3	3
4	1	4	4	4
3	12	5	5	5
2	15	6	6	6
1	14	VCA level (2 Voice)	PWM CV (2 Voice)	Resonance (2 Voice)
0	13	VCA level (4 Voice)	PWM CV (4 Voice)	Resonance (4 Voice)
	range	0 ~ -10V	+4 ~ -6V	0 ~ +10V
	V/V converter	072 IC21(b)	M5218P IC20(A)	M5218P IC20(b)

<Table 9 DMUX data>

d) Wave generator



<Fig. 11 MC5534A>

MC5534A IC15 is, with a given rectangular at CLK X (CLK Y), capable of generating three different waveforms; divided by two rectangular, sawtooth and variable-width rectangular (Pulse Width Modulated).

[SUB OSCILLATOR]

Output of the Sub Oscillator is generated by dividing the DCO output frequency that is supplied to CLKX and CLKY by two at D-F/F in Wave Generator IC15. The amplitude of this output varies in four steps by changing the collector voltage Q3 and Q6 with the output of 2-bit D/A converter IC28.

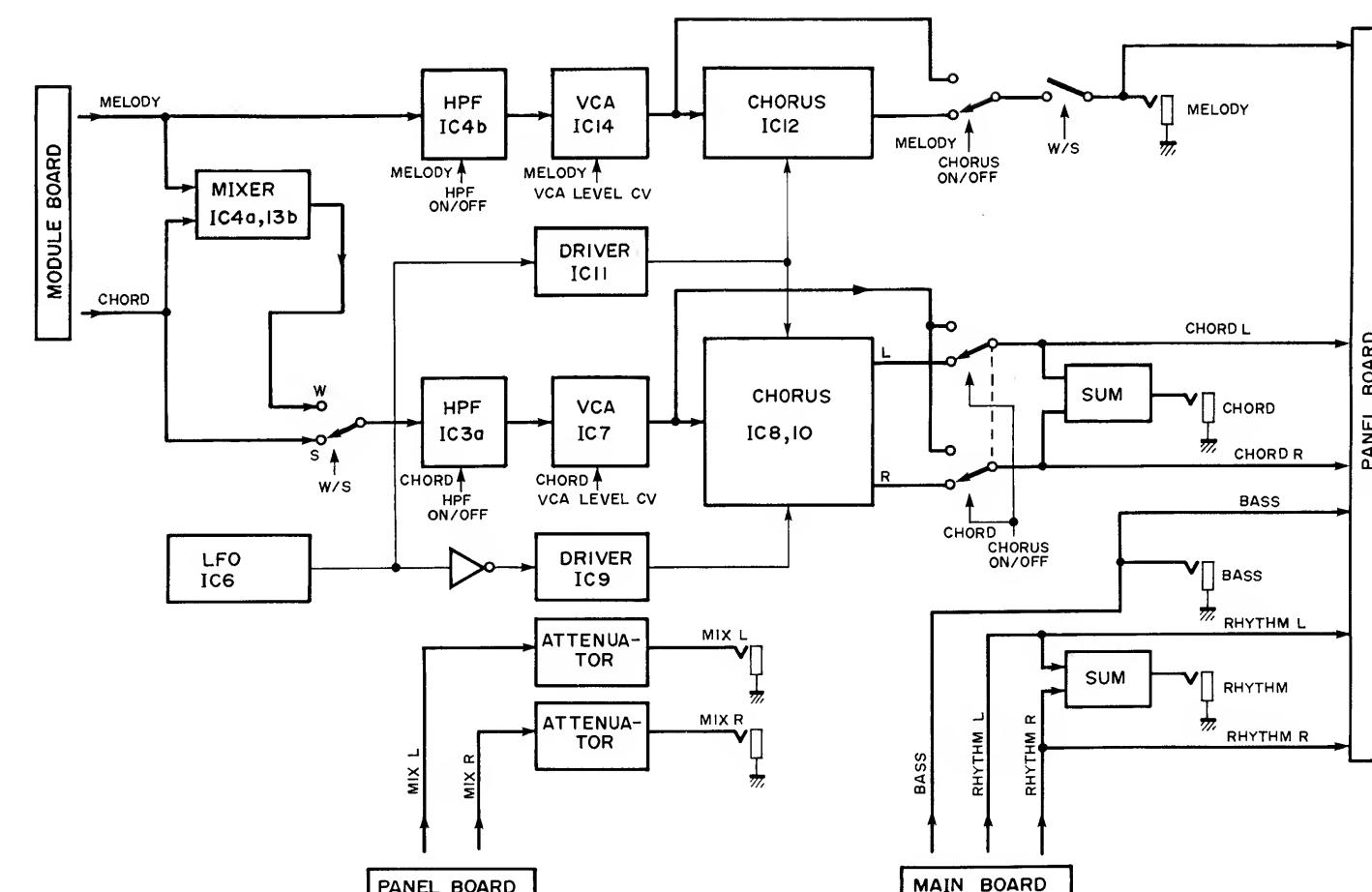
[SAWTOOTH]

Miller integrating circuit in IC15 generates a sawtooth wave at DCO output rate with its amplitude being kept constant over the frequency range by DCO CV.

[PULSE MODULATED WAVE]

With sawtooth wave and PWM CV applied at input pins, the comparator in IC15 develops a square wave whose duty cycle will vary 50-95% in response to PWM CV levels. Duty cycle is 50% at +6V PWM CV and 95% at +0.6V. With PWM OFF, PWM CV is -1V; this can swing and keep comparator output to High, disabling the rectangular.

JACK BOARD Block Diagram



<Fig. 12 JACK BOARD Block Diagram>

e) VCF, VCA

A1QH80017A IC14, 16 is a one-chip VCF and VCA. Both VCF and VCA are individually controlled by the several parameters integrated into one voltage: VCF CV contains CUTOFF frequency, ENV, LFO and Key follow; VCA CV includes ENV and GATE.

ADJUSTMENT

ENGINEER MODE

Engineer Mode is used to edit each parameter of the MELODY, CHORD, and BASS blocks on the synthesizer.

● ENTERING ENGINEER MODE

While holding down one of Sound Source Selector buttons — MELODY, CHORD or BASS to be edited, depress MIDI CH. The button flashes, indicating that the machine is in the Engineer Mode.

● EDITING

1. Using Tables 1 and 2, find out the number corresponding to a parameter to be altered. Enter the number on the numerical pad. The display shows the number.

2. DYNAMICS SENS serves as an edit knob, and the new number is represented in the display window as a number between 00 and 127. The numbers above 100 are indicated in lower two digits. (e.g. 123=23)

In the case where parameters functioning in place of switch, a parameter can effectively open or close the switch when the value exceeds the predetermined figure. For example, CHORUS is turned ON when the value decreases below 63, and OFF when above 64.

● RETURNING TO NORMAL MODE

Push the MIDI channel Button to Normal functioning.

エンジニア・モード

エンジニア・モードは、メロディー、コード、ベース・ブロックについてシンセサイザーの各パラメーターをエディットするモードです。

● エンジニア・モードへの入り方

電源ONの状態にてメロディー、コード、ベースのうち、修正したいブロックの音源セレクト・ボタンを押しながらMIDIチャンネル・ボタンを押します。指定した音源セレクト・ボタンが点滅します。

● エディットの方法

1. エディットしたいパラメータに対応した番号を Table 1. 2 より探し、ナンバー・ボタンで選択します。

選択したパラメータ・ナンバーがディスプレイに表示されます。

2. ダイナミクス・センシティビティ・ツマミがエディット・ツマミとして動作しますので好みの位置に調節します。エディット時のディスプレイは設定値表示になります。

(最少: 00、最大127 ただし100のケタは省略され2ケタ表示になります。例 123=23)

スイッチ動作のパラメータ、例えばコーラスの場合には、63以下が“ON” 64以上が“OFF”に設定されています。

● ノーマル・モードへの戻り方

MIDIチャンネル・ボタンを押します。

No.	PARAMETER	No.	PARAMETER
01	LFO	14	ENV
02		15	Dynamic select
03	DCO	17	Range
04		18	PWM
05	VCF	19	Rectangular
06		20	Sawtooth
07		21	Sub
08		22	HPF
09		23	VCF
10		24	VCA
11		25	Chorus
12	ENV	26	Noise (MELODY)
13			Sustain level

<Table 1 MELODY, CHORD BLOCK PARAMETER>

No.	PARAMETER
04	DCO
05	
06	
07	
09	
10	VCA
11	
12	
13	
14	
20	Wave select

<Table 2 BASS BLOCK PARAMETER>

TEST MODE

Test, Adjust and Inspection programs run only in the Test Mode.

● ENTERING THE TEST MODE

While pressing button No. 3, switch the power ON. The display shows — **Adj** (Adj.)

● FOUR FUNCTIONS IN THE TEST MODE

Each of MELODY, CHORD, BASS, and RHYTHM buttons serves as a function selector button.

MELODY UNISON BASS BASS
CHORD ROTARY RHYTHM RHYTHM

1. UNISON (MELODY)

In this mode the unit outputs all the 6 voices simultaneously from the CHORD and MELODY channels.

The display flashes **U-**, indicating that the default test program is D/A OFFSET Adj.

To change the adjusting item, select the corresponding number on the numerical pad.

Display	TEST PROGRAM	
0 -	Adjustment	D/A offset
1 -	Adjustment	VCA offset
2 -	Adjustment	VCF Resonance, Frequency and VCA Gain
3 -	Adjustment	VCF Width
4 -	Adjustment	□ duty cycle 50%
	Inspection	□ duty cycle 95%
5 -	Inspection	／＼ level (Range)
6 -	Inspection	／＼ level (Key scale)
7 -	Inspection	□ duty cycle 50% (Key scale)
8 -	Inspection	Sub OSC level
9 -	Inspection	Noise ON/OFF

<Table 3 UNISON TEST PROGRAM>

2. ROTARY (CHORD)

In this mode one module is activated at a time with MELODY or CHORD is selected.

The display flashes **Q1**. The left digit indicates test item and the right digit the module being sounded.

To change the test item, select the number from the numerical pad. To change the module to the next channel, press BASS, CHORD or MELODY.

2. ロータリー (コード・ボタン)

メロディー、コードの6モジュールのうち1モジュールずつ順番に発音するモードです。ディスプレイは **Q1** が点滅します。テスト・プログラムは7通りあり、ナンバー・ボタンを押すことで変化します。テスト・プログラムの番号は、ディスプレイの左のケタに表示されます。発音しているモジュールは音源セレクト・ボタンのどれかを押すつど次のモジュールに変わっていきます。発音しているモジュール・ナンバーはディスプレイの右のケタに表示されます。

Display	TEST PROGRAM	
0 X	Inspection	／＼ Level
1 X	Inspection	□ Level
2 X	Inspection	PWM LFO
3 X	Inspection	VCF low frequency
4 X	Inspection	VCF high frequency
5 X	Inspection	HPF ON/OFF
6 X	Inspection	CHORUS ON/OFF

<Table 4 ROTARY TEST PROGRAM>

3. BASS (BASS)

This mode is used for testing BASS. The display flashes **b0**. Select a test item on the numerical pad.

3. ベース (ベース・ボタン)

ベース用のモードです。ディスプレイは **b0** が点滅します。ディスプレイはナンバー・ボタンを押すことで変化し、0-8 の9通りのテスト・プログラムを表示します。

Display	TEST PROGRAM	
b 0	Adjustment	VCA offset
b 1	Adjustment	ENV Attack time
b 2	Inspection	VCF low frequency
b 3	Inspection	VCF high frequency
b 4	Inspection	VCF ENV/Dynamics (40H)
b 5	Inspection	VCF ENV/Dynamics (7FH)
b 6	Inspection	VCA Level
b 7	Inspection	VCF Width
b 8		

<Table 5 BASS TEST PROGRAM>

テスト・モード

テスト・モードは調整、検査を行なう時に使用するモードです。このモードでは調整検査用にプリセットされているセッティングを呼び出すことができます。

● テスト・モードへの入り方

ナンバー・ボタン “3” を押しながら電源を投入します。ディスプレイに **Adj** (Adj.) と表示されます。

● テスト・モードの4つのモード

各ブロックのボタン (メロディー、コード、ベース、リズム) がモード・セレクト・ボタンになります。以下に各モードを説明します。

1. ユニゾン (メロディー・ボタン)

メロディー、コードの全モジュール (6音) が同時に発音するモードです。ディスプレイは **U-** が点滅します。ディスプレイはナンバー・ボタンを押すことで変化します。テスト・プログラムは10通りのテストプログラムを表示します。

4. RHYTHM (RHYTHM)

Rhythm circuits can be checked in this mode. The display flashes  upon pressing RHYTHM. Select the voice using numerical pad. The voice sounds once for each tap on the numerical pad. Dynamics level can be varied from DYNAMICS SENS.

Button	VOICE
0	RIDE CYMBAL
1	BASS DRUM
2	SNARE DRUM
3	LOW-TOM
4	MID-TOM
5	HI-TOM
6	RIM SHOT
7	HAND CLAP
8	CLOSED HI-HAT
9	OPEN HI-HAT
FUNCTION	CLASH CYMBAL

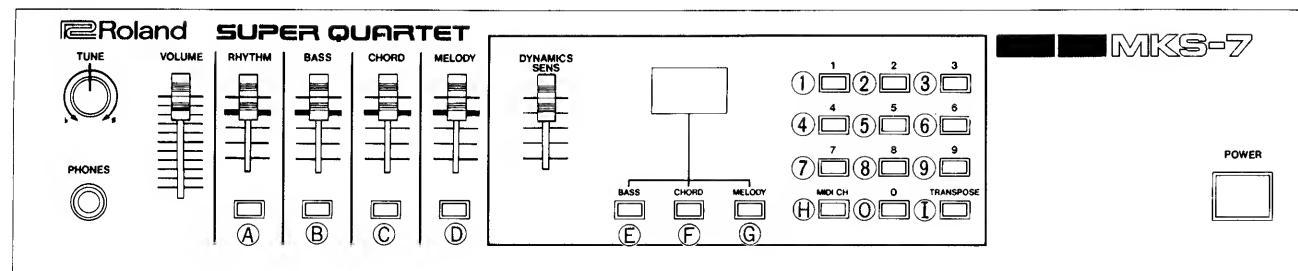
<Table 6 RHYTHM TEST PROGRAM>

TO RETURN TO THE NORMAL MODE, switch the power OFF and ON again.

●ノーマル・モードへの戻り方
電源をいったん切り、ふたたび投入します。

ADJUSTMENT

With Test Mode Set the controls as shown below (initial setting). In these adjustments, each name of the switches is called by the name shown in Fig. 1.



調整

テスト・モードでのつまみの初期設定はFig.1のようになります。
この章では、各スイッチ名をFig.1のよう呼びことにします。

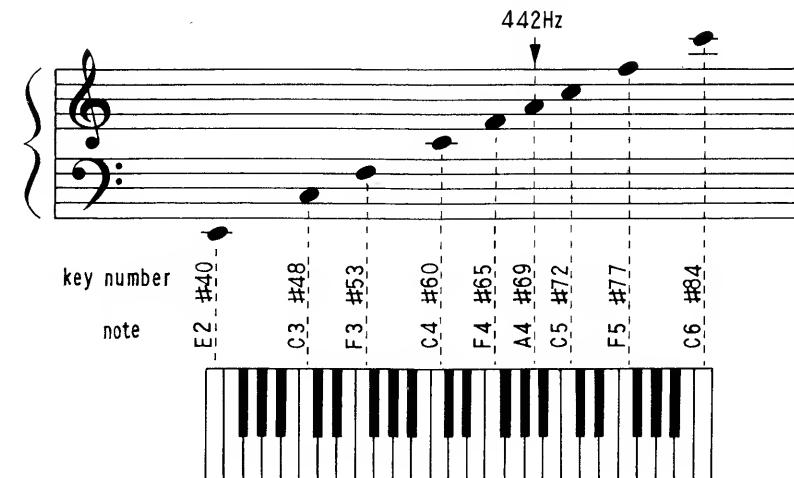


<Fig. 1>

Allow at least ten minutes for warm-up.
調整、検査は最低10分の通電後始めて下さい。

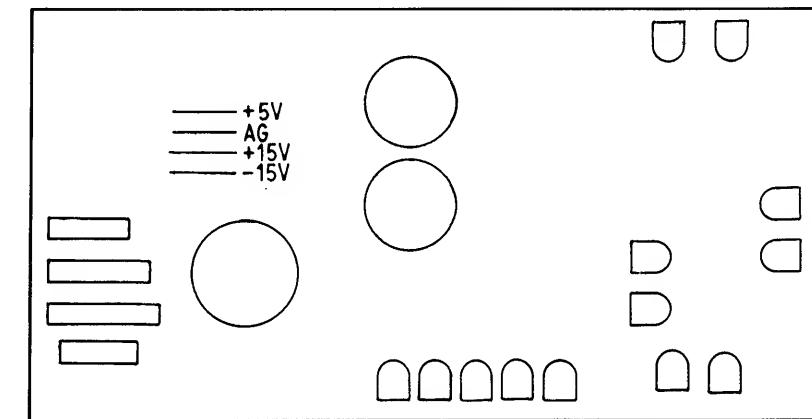
On power up the MKS-7 Keyboard is assigned notes as follows.

MKS-7 がイニシャライズされた時、ノートとキー・ナンバーの関係はFig.2の通りです。



<Fig. 2>

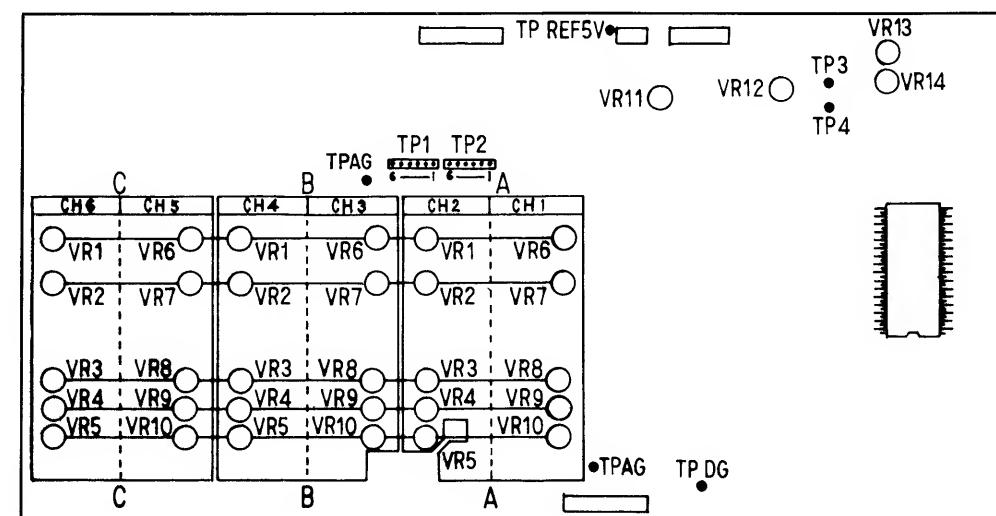
1. POWER SUPPLY BOARD



<Fig. 3 Power Supply Board>

	MEASURING INSTRUMENT	ITEM	MODE/CONTROL	DISPLAY	TEST POINT (GND)	ADJUST/CHECK for	READING
1	Digital voltmeter	+ 5V	INITIAL TEST MODE	Rd	+ 5V Jumper (AG Jumper)	Check	+5.0V ± 0.2V
2		+ 15V		+ 15V Jumper	+15.0V ± 0.6V		
3		-15V		-15V Jumper	-15.0V ± 0.6V		

2. MODULE BOARD (MELODY, CHORD BLOCK)



<Fig. 4 Module Board>

The Program runs only in the TEST MODE. Proceed in numerical order.

この調整、検査はテスト・モードでのみ行なえます。項目1から19まで順番に進んでください。

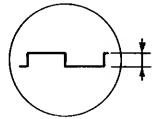
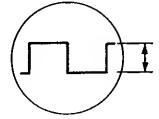
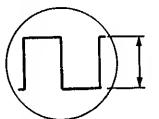
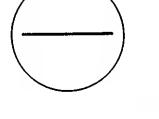
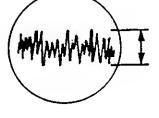
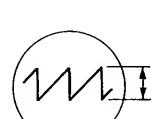
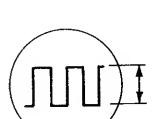
	MEASURING INSTRUMENT	ITEM	MODE/CONTROL	DISPLAY	TEST POINT (GND)	ADJUST/ CHECK for	READING
1	Digital voltmeter	REF 5V	INITIAL TEST MODE	Rd	TP REF5V	(TP AG)	VR14 +5.000V ± 5mV
2		D/A offset	Push ①	0-	TP3		VR13 0.000V ± 5mV
3	Oscilloscope 0.02 v/div 2 ms/div	VCA offset	Push ①	/-	TP2-1		VR14 +5.000V ± 5mV
					TP2-2		VR13 0.000V ± 5mV
					TP2-3		VR6A
					TP2-4		VR1A
					TP2-5		VR6B
					TP2-6		VR1B
					TP2-6		VR6C
4	Oscilloscope 1 v/div 1 ms/div	VCF Resonance	Push ②	/-	TP1-1		VR14 +5.000V ± 5mV
					TP1-2		VR13 0.000V ± 5mV
					TP1-3		VR6A
					TP1-4		VR1A
					TP1-5		VR6B
					TP1-6		VR1B
5					TP2-1		VR6C
					TP2-2		VR1C
					TP2-3		VR10A
					TP2-4		VR5A
					TP2-5		VR10B
					TP2-6		VR5B
6	Frequency counter (Tuner)	VCF Frequency	Push ③	/-	TP1-1		VR14 +5.000V ± 5mV
					TP1-2		VR13 0.000V ± 5mV
					TP1-3		VR6A
					TP1-4		VR1A
					TP1-5		VR6B
					TP1-6		VR1B
7					TP2-1		VR6C
					TP2-2		VR1C
					TP2-3		VR10A
					TP2-4		VR5A
					TP2-5		VR10B
					TP2-6		VR5B
8	Oscilloscope 1 v/div	Push ④	/-	(TPAG)	TP2-1		VR14 +5.000V ± 5mV
					TP2-2		VR13 0.000V ± 5mV
					TP2-3		VR6A
					TP2-4		VR1A
					TP2-5		VR6B
					TP2-6		VR1B
9					TP2-1		VR6C
					TP2-2		VR1C
					TP2-3		VR10A
					TP2-4		VR5A
					TP2-5		VR10B
					TP2-6		VR5B
10	Oscilloscope 1 v/div 1 ms/div	Push ⑤	/-	(TPAG)	TP2-1		VR14 +5.000V ± 5mV
					TP2-2		VR13 0.000V ± 5mV
					TP2-3		VR6A
					TP2-4		VR1A
					TP2-5		VR6B
					TP2-6		VR1B

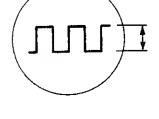
Items 6 and 7 interact. Repeat both items until satisfactory results are obtained.

項目6、7は相互に影響します。項目6、7が共にあうまで調整をくり返して下さい。

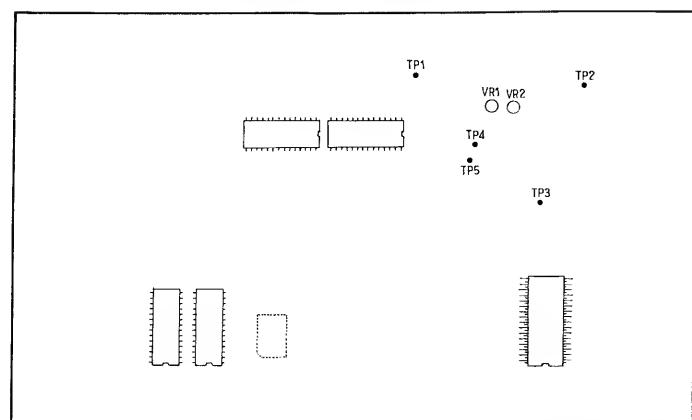
8	Oscilloscope 1 v/div	Push ④	/-	TP2-1	VR12 50% ± 2%		VR14 +5.000V ± 5mV
				TP2-2	VR11 Check		VR13 0.000V ± 5mV
9	Push ⑥ consecutively 2 times ⑥ボタンを2回押す	Push ④	/-	TP2-1	95% ± 3%		VR14 +5.000V ± 5mV
							VR13 0.000V ± 5mV
10	Oscilloscope 1 v/div 1 ms/div	Push ⑤	/-	(TPAG)	TP2-1		VR14 +5.000V ± 5mV
							VR13 0.000V ± 5mV
							VR6A
							VR1A
							VR6B
							VR1B

	MEASURING INSTRUMENT	ITEM	MODE/CONTROL	DISPLAY	TEST POINT (GND)	ADJUST/ CHECK for	READING
10	Oscilloscope 1 v/div 1 ms/div	Push ⑥ consecutively 2 times ⑥ボタンを2回押す	/-	TP2-1	TP2-1	Check	4.8Vp-p ± 0.5V
		Push ⑥	TP2-1	TP2-2	TP2-3	TP2-4	TP2-5
11	Oscilloscope 1 v/div 2 ms/div	Push ⑥ consecutively 2 times ⑥ボタンを2回押す	/-	TP2-1	TP2-1	TP2-2	TP2-3
12	Oscilloscope 1 v/div	Push ⑦	/-	TP2-1	TP2-1	TP2-2	TP2-3
		Push ⑦	TP2-1	TP2-2	TP2-3	TP2-4	TP2-5
13	Oscilloscope 1 v/div 1 ms/div	Push ⑧	/-	TP2-1	TP2-1	TP2-2	TP2-3
		Push ⑧	TP2-1	TP2-2	TP2-3	TP2-4	TP2-5

	MEASURING INSTRUMENT	ITEM	MODE/CONTROL	DIS-PPLAY	TEST POINT (GND)	ADJUST/CHECK for	READING
13	Oscilloscope 1 v/div 1 ms/div	Sub OSC. level	Push ⑥	8-	TP2-1 TP2-2 TP2-3 TP2-4 TP2-5 TP2-6	(TP AG)	Check 1.5Vp-p ± 0.1V 
			Push ⑥		TP2-1 TP2-2 TP2-3 TP2-4 TP2-5 TP2-6		3.5Vp-p ± 0.2V 
			Push ⑥		TP2-1 TP2-2 TP2-3 TP2-4 TP2-5 TP2-6		5.5Vp-p ± 0.3V 
14	NOISE ON/OFF	Push ⑨	9-	TP2-1 TP2-2 TP2-3 TP2-4 TP2-5 TP2-6	TP2-1 TP2-2 TP2-3 TP2-4 TP2-5 TP2-6	0Vp-p Approx. 3Vp-p	0Vp-p  Approx. 3Vp-p 
			Push ⑥		TP2-3 TP2-4 TP2-5 TP2-6		
					TP2-1 TP2-2		
15	Oscilloscope 1 v/div 1 ms/div	▲level	Push ⑩	01	MIX OUTPUT L HOT (MIX OUTPUT L COLD)		3.8Vp-p ± 0.4V 
			Push ⑥	02			2.4Vp-p ± 0.3V 
			Push ⑥	03			
			Push ⑥	04			
			Push ⑥	05			
			Push ⑥	06			
16		□ level	Push ⑪	11			3Vp-p ± 0.3V 
			Push ⑥	12			

	MEASURING INSTRUMENT	ITEM	MODE/CONTROL	DIS-PPLAY	TEST POINT (GND)	ADJUST/CHECK for	READING
16	Oscilloscope 1 v/div 1 ms/div	□ level	Push ⑬	13	MIX OUTPUT L HOT (MIX OUTPUT L COLD)	Check	2.0Vp-p ± 0.2V 
			Push ⑬	14			
			Push ⑬	15			
			Push ⑬	16			
17	Oscilloscope 1 v/div 0.5 ms/div	PWM LFO	Push ⑫	21			
			Push ⑬	22			
			Push ⑬	23			
			Push ⑬	24			
			Push ⑬	25			
			Push ⑬	26			
18	Oscilloscope 1 v/div 20 ms/div	VCF low frequency	Push ⑬	31			
			Push ⑬	32			
			Push ⑬	33			
			Push ⑬	34			
			Push ⑬	35			
			Push ⑬	36			
19	Oscilloscope 0.5V/div 5 μs/div	VCF high frequency	Push ⑭	41			
			Push ⑬	42			
			Push ⑬	43			
			Push ⑬	44			
			Push ⑬	45			
			Push ⑬	46			

3. MAIN BOARD (BASS BLOCK)



<Fig. 5 Main Board>

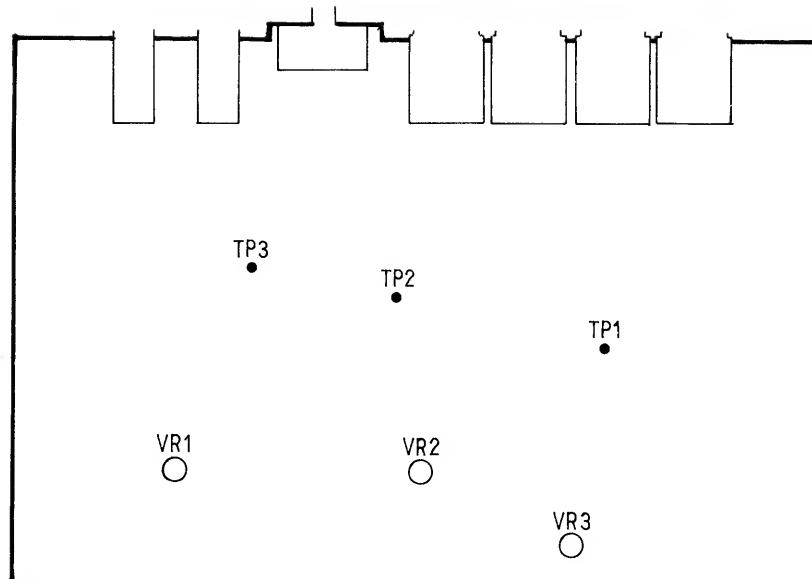
The Program runs only in the TEST MODE. Proceed in numerical order.

この調査、検査はテスト・モードでのみ行なえます。項目1から9まで順番に進んでください。

	MEASURING INSTRUMENT	ITEM	MODE/CONTROL	DISPLAY	TEST POINT (GND)	ADJUST/ CHECK for	READING
1	Oscilloscope 0.2 v/div 2 ms/div	VCA offset	Push ⑥	b6	TP5 (TP1)	VR1	
2	Oscilloscope 2 v/div 0.5 s/div	ENV Attack time	Push ① Holding down ⑤	b7	TP4	VR2	
3	Oscilloscope 2 v/div 10 ms/div	VCF low frequency	Push ②	b8	TP5	Check	
4	Oscilloscope 2 v/div 5 μs/div	VCF high frequency	Push ③	b9			
5	Oscilloscope 2 v/div 1 ms/div	VCF ENV/ Dynamics (40H)	Push ④	b10			
6	Oscilloscope 2 v/div 20 μs/div	VCF ENV/ Dynamics (7FH)	Push ⑤	b11			

	MEASURING INSTRUMENT	ITEM	MODE/CONTROL	DISPLAY	TEST POINT (GND)	ADJUST/ CHECK for	READING
7	Oscilloscope 1 v/div 2 ms/div	VCA level	Push ⑥	b6	TP5 (TP1)	Check	4Vp-p ± 0.5V
8	Oscilloscope 2 v/div 0.5ms/div	VCF Width	Push ⑦	b7		Oscilloscope Time variable knob	10 div
			Push ⑧	b8		Check	4.5div ± 1.5div
9	Oscilloscope 1 v/div 2 ms/div	VCA level	Push ⑥	b6	MIX OUTPUT L HOT (MIX OUTPUT L COLD)		2.2Vp-p ± 0.4V

4. JACK BOARD



<Fig. 6 Jack Board>

a) BBD Bias 1

Setting: (with NORMAL MODE)

1. Set the tone color of MELODY block to No. 99.
2. Set into Engineer Mode of the MELODY block.
3. Select VCF Resonance (06), and then move DYNAMICS SENS to get the value 127.
4. Select VCA Level (10), and then move DYNAMICS SENS to get the value 127.
5. Select ENV Sustain (13), and then move DYNAMICS SENS to get the value 127.
6. Select the VCF Cut-off frequency (05), and then move DYNAMICS SENS to get the value 75.

Test instrument:

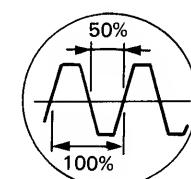
Oscilloscope 2V/div. 0.1 mS/div.

Test point:

TP1, GND – Module Board AG

Adjustment:

While holding down the Button D, adjust VR3 for a 50% duty cycle.



a) BBD Bias 1 :

セッティング: (ノーマル・モード)

1. メロディー・ブロックの音色を99番にします。
2. メロディー・ブロックのエンジニア・モードに入ります。
3. VCF レゾナンス (06) を選びダイナミクス・センシティビティー・ツマミで設定値を127にします。
4. VCA レベル (10) を選びダイナミクス・センシティビティー・ツマミで設定値を127にします。
5. ENV サスティーン (13) を選びダイナミクス・センシティビティー・ツマミで設定値を127にします。
6. VCF カットオフ・フレケンシー (05) を選びダイナミクス・センシティビティー・ツマミで75にします。

使用機器:

オシロ・スコープ 2V/div 0.1ms/div

テスト・ポイント:

TP1 (GND: モジュール・ボード AG)

調整方法:

ボタン①を押したまま波形のデューティー・サイクルが50%になるようにVR3で調整します。

b) BBD Bias 2, 3

Setting:

1. Set the tone color of the CHORD block to No. 99.
2. Set into Engineer Mode of the CHORD block.
3. Select VCF Resonance (06), and then move DYNAMICS SENS to get the value 127.
4. Select VCA Level (10), and then move DYNAMICS SENS to get the value 127.
5. Select ENV Sustain (13), and then move DYNAMICS SENS to get the value 127.
6. Select VCF cut-off frequency (05), and then move DYNAMICS SENS to get the value 75.

Test instrument:

Oscilloscope 2V/div. 0.1mS/div.

Test point:

BBD Bias 2 – TP2, GND – Module Board AG

BBD Bias 3 – TP3, GND – Module Board AG

Adjustment:

BBD Bias 2:

While holding down the Button C, adjust VR2 for a 50% duty cycle.

BBD Bias 3:

While holding down the Button C, adjust VR1 for a 50% duty cycle.

b) BBD Bias 2, 3

セッティング:

1. コード・ブロックの音色を99番にします。
2. コード・ブロックのエンジニア・モードに入ります。
3. VCF レゾナンス (06) を選びダイナミクス・センシティビティー・ツマミで設定値を127にします。
4. VCA レベル (10) を選びダイナミクス・センシティビティー・ツマミで設定値を127にします。
5. ENV サスティーン (13) を選びダイナミクス・センシティビティー・ツマミで設定値を127にします。
6. VCF カットオフ・フレケンシー (05) を選びダイナミクス・センシティビティー・ツマミで75にします。

使用機器:

オシロ・スコープ 2V/div 0.1ms/div

テスト・ポイント:

BBD Bias 2 : TP2 (GND: モジュール・ボード AG)

BBD Bias 3 : TP3 (GND: モジュール・ボード AG)

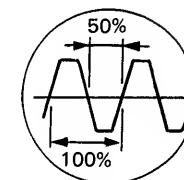
調整方法:

BBD Bias 2 :

ボタン②を押したまま波形のデューティー・サイクルが50%になるようにVR2で調整します。

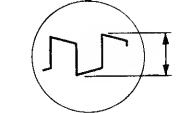
BBD Bias 3 :

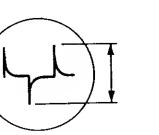
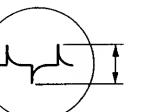
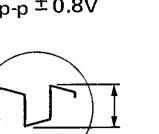
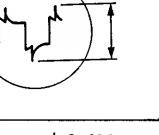
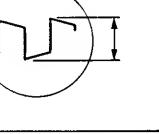
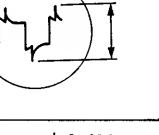
ボタン②を押したまま波形のデューティー・サイクルが50%になるようにVR1で調整します。



The Program runs only in the TEST MODE. Proceed in numerical order.

この調整、検査はテスト・モードでのみ行なえます。項目1から2まで順番に進んでください。

ITEM	MEASURING INSTRUMENT	MODE/CONTROL	DISPLAY	TEST POINT (GND)	ADJUST/CHECK for	READING
1	HPF Oscilloscope 1 v/div 2 ms/div	INITIAL TEST MODE	Rd	MIX OUTPUT L HOT (MIX OUTPUT L COLD)	Check	4Vp-p ± 0.8V 
		Push ④	④			
		Push ⑤	⑤			
		Push ⑥ consecutively 2 times	⑥			
		Push ⑦ consecutively 2 times	⑦			
		Push ⑧ consecutively 2 times	⑧			
		Push ⑨ consecutively 2 times	⑨			
		Push ⑩ consecutively 2 times	⑩			

	MEASURING INSTRUMENT	ITEM	MODE/CONTROL	DISPLAY	TEST POINT (GND)	ADJUST/ CHECK for	READING
1	Oscilloscope 1 v/div 2 ms/div	HPF	Push ⑥ consecutively 3 times	51	MIX OUTPUT L HOT (MIX OUTPUT L COLD)	Check	5.5Vp-p ± 1.5V
			Push ⑥ consecutively 2 times	52			
			Push ⑥ consecutively 2 times	53			3.6Vp-p ± 0.8V
			Push ⑥ consecutively 2 times	54			
			Push ⑥ consecutively 2 times	55			
			Push ⑥ consecutively 2 times	56			
2	CHORUS	Push ⑥		b1		Check	4Vp-p ± 0.8V
							
		Push ⑥					Approx. 6Vp-p
							
		Push ⑥ consecutively 3 times		b3			2.5Vp-p ± 0.4V
		Push ⑥					Approx. 4Vp-p

1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 |

A

B

C

D

E

F

G

H

I

J

K

L

M

N

O

P

Q

R

S

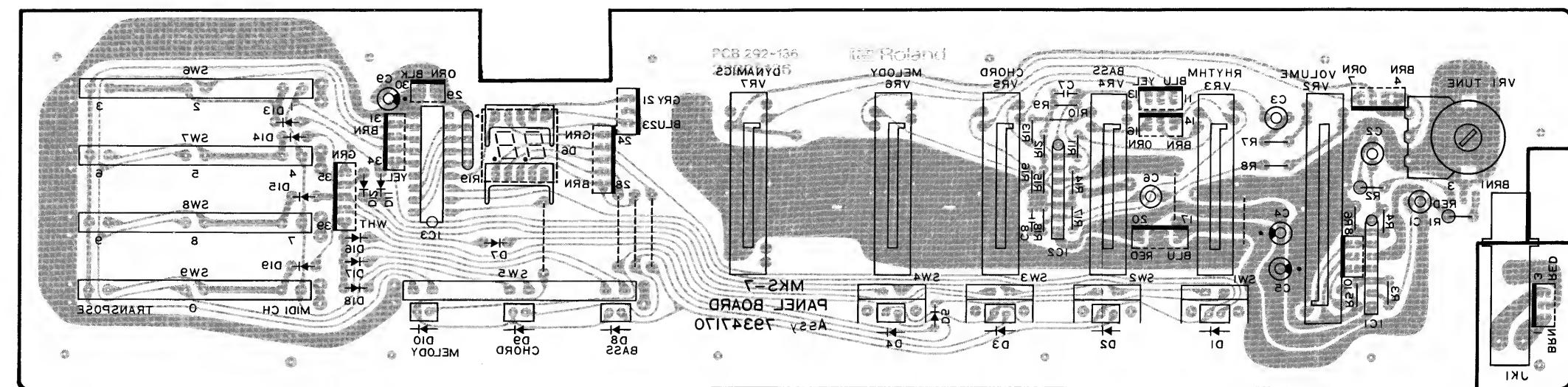
T

PANEL BOARD

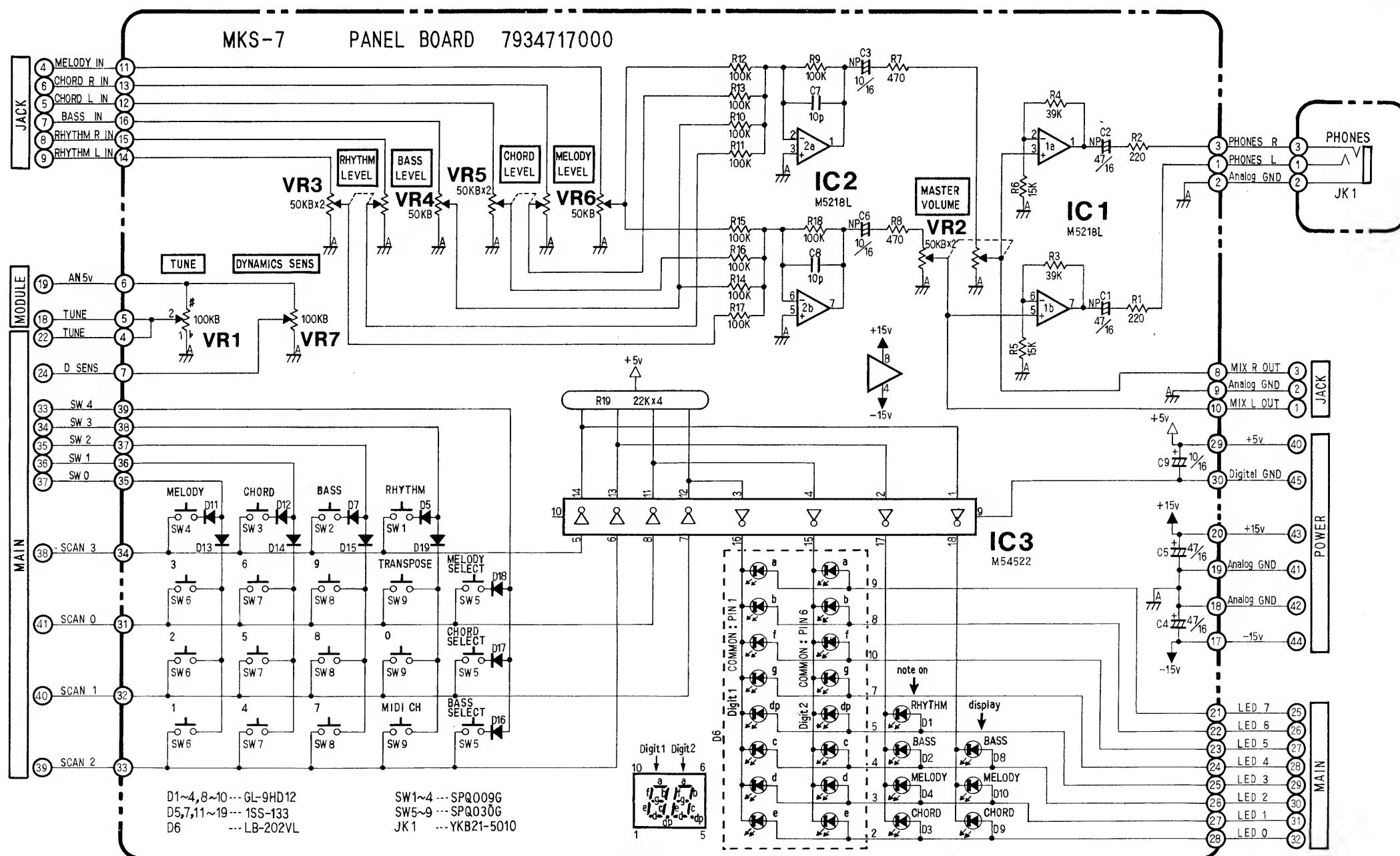
7934717000

[pcb 22925136]

COLOR	SERIAL No.	R9-R18	C7, C8
BLACK	520100-550549	33kΩ	22pF
IVORY	540400-550249		
BLACK	560550-UP	100kΩ	10pF
IVORY	560250-UP		

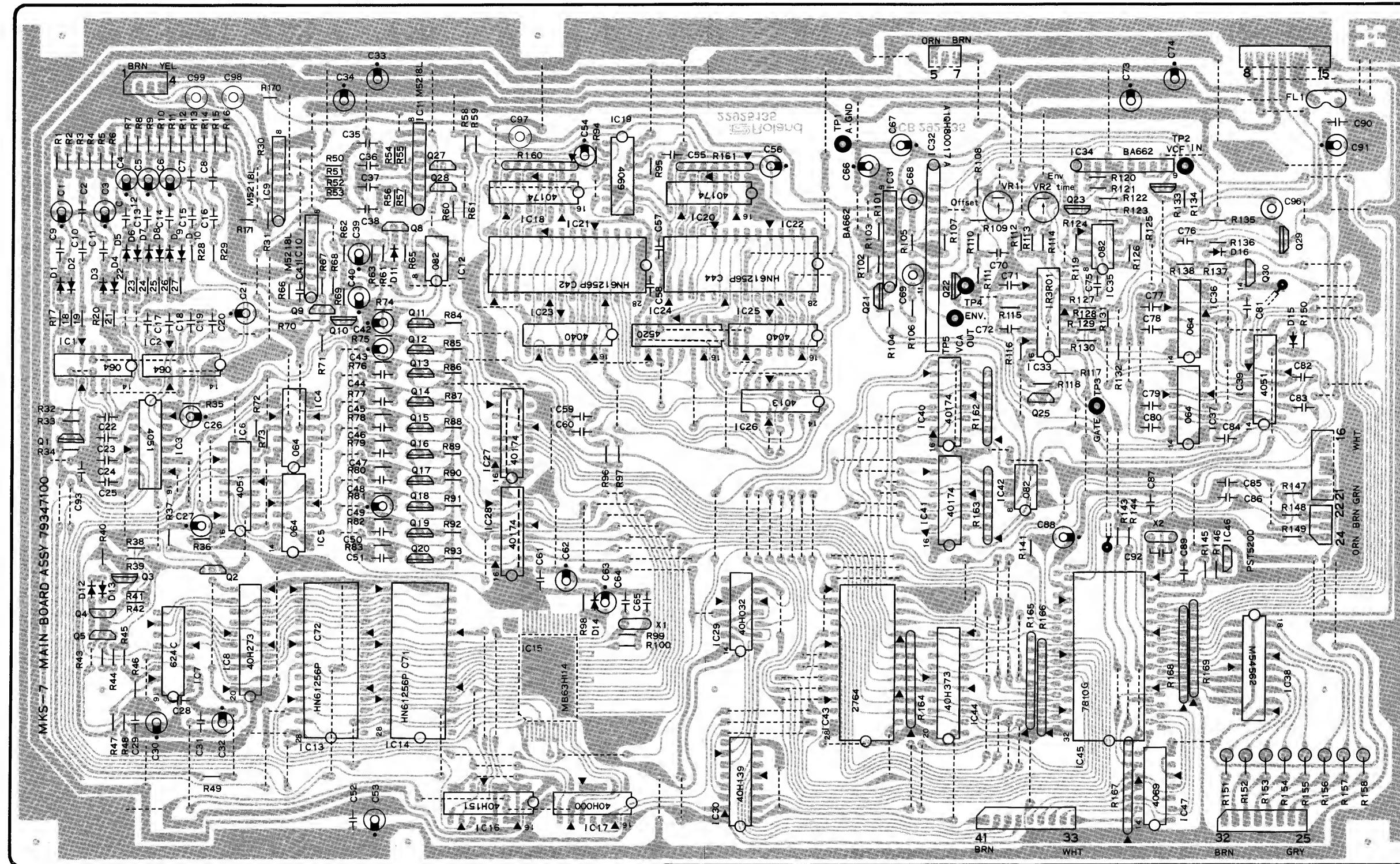


View from foil side.



1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 |

MAIN BOARD 7934710000 (pcb 22925135)

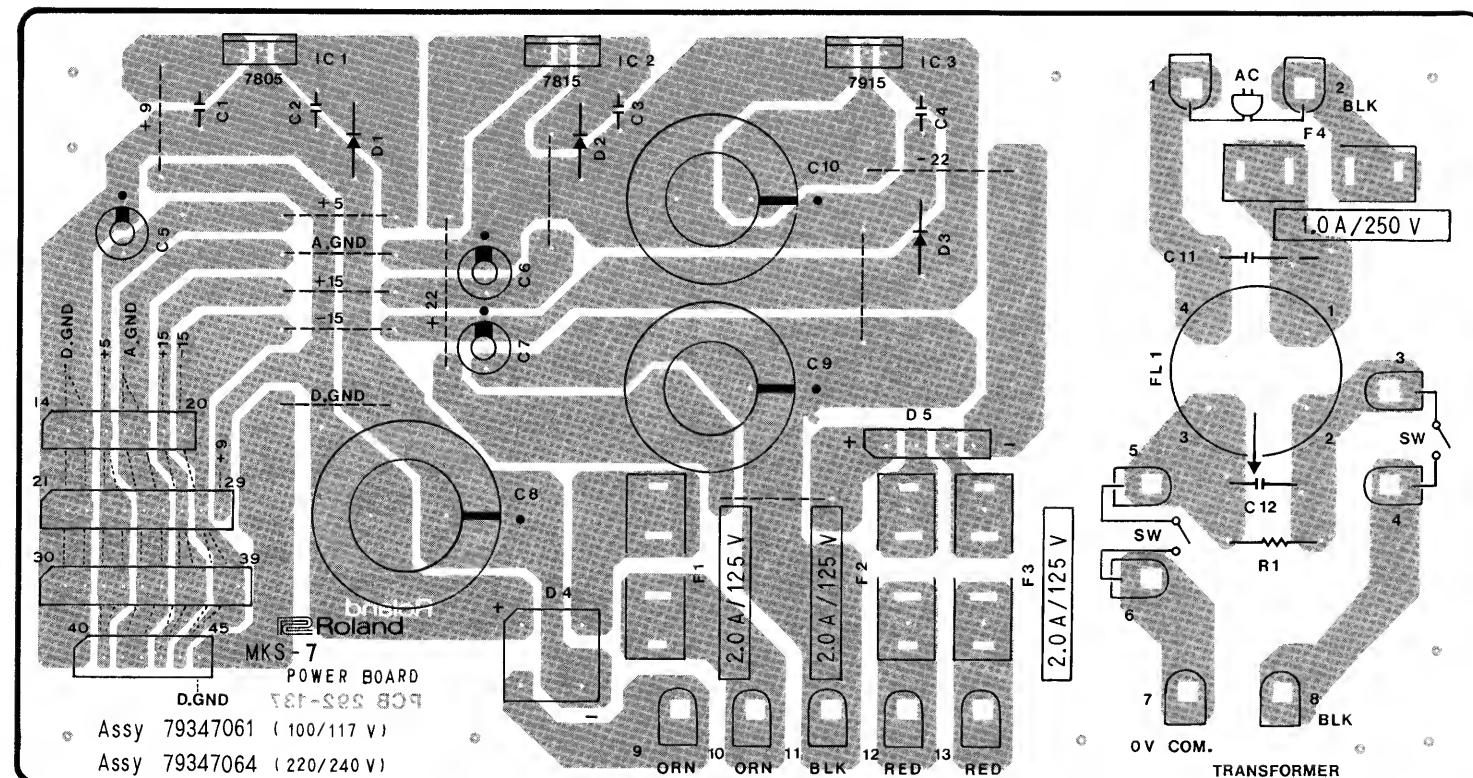


View from component side.

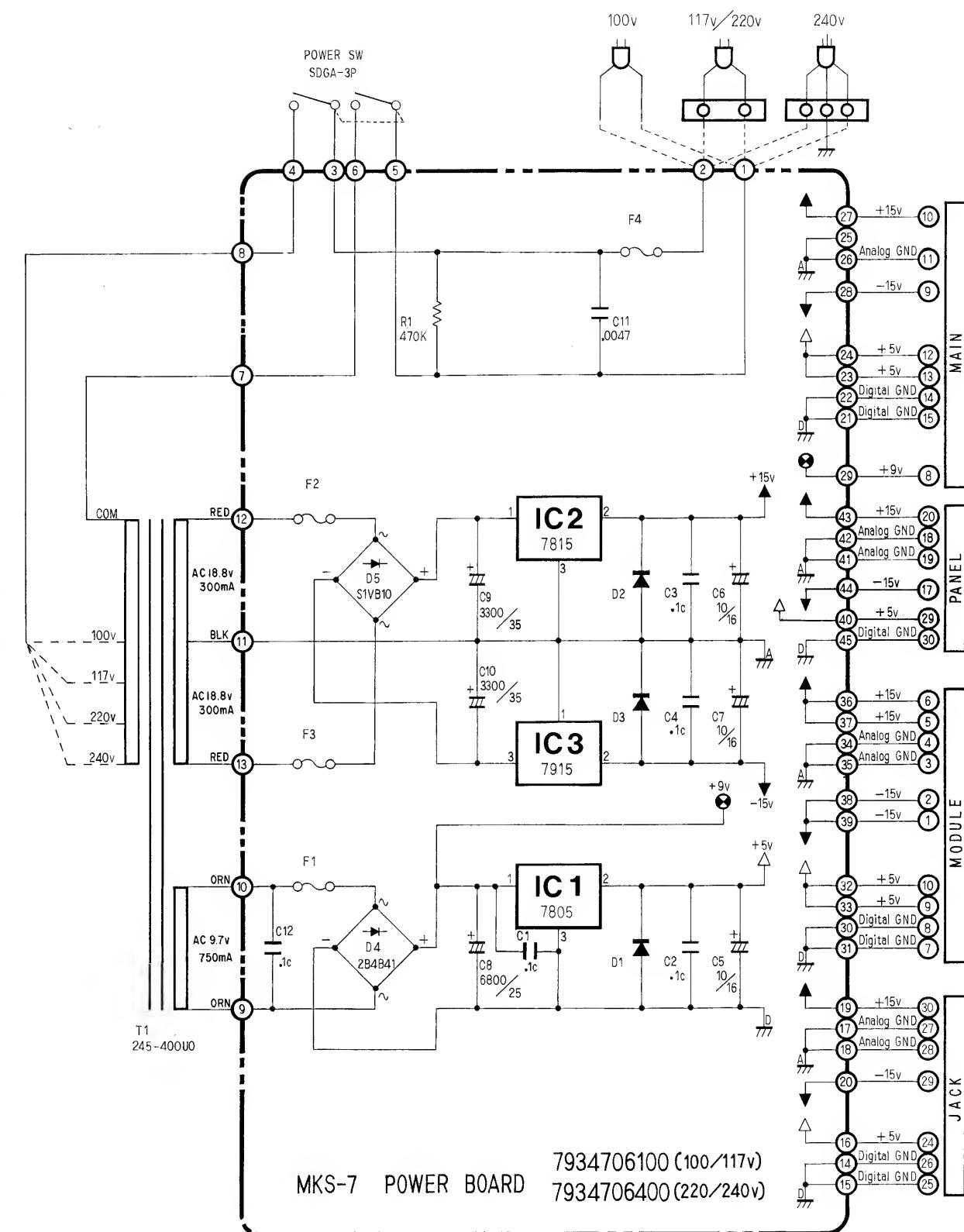
1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 |

POWER SUPPLY BOARD

7934706100 100/117V (pcb 22925137)
7934706400 220/240V (pcb 22925137)



View from component side



MKS-7 POWER BOARD

7934706100 (100/117v)
7934706400 (220/240v)

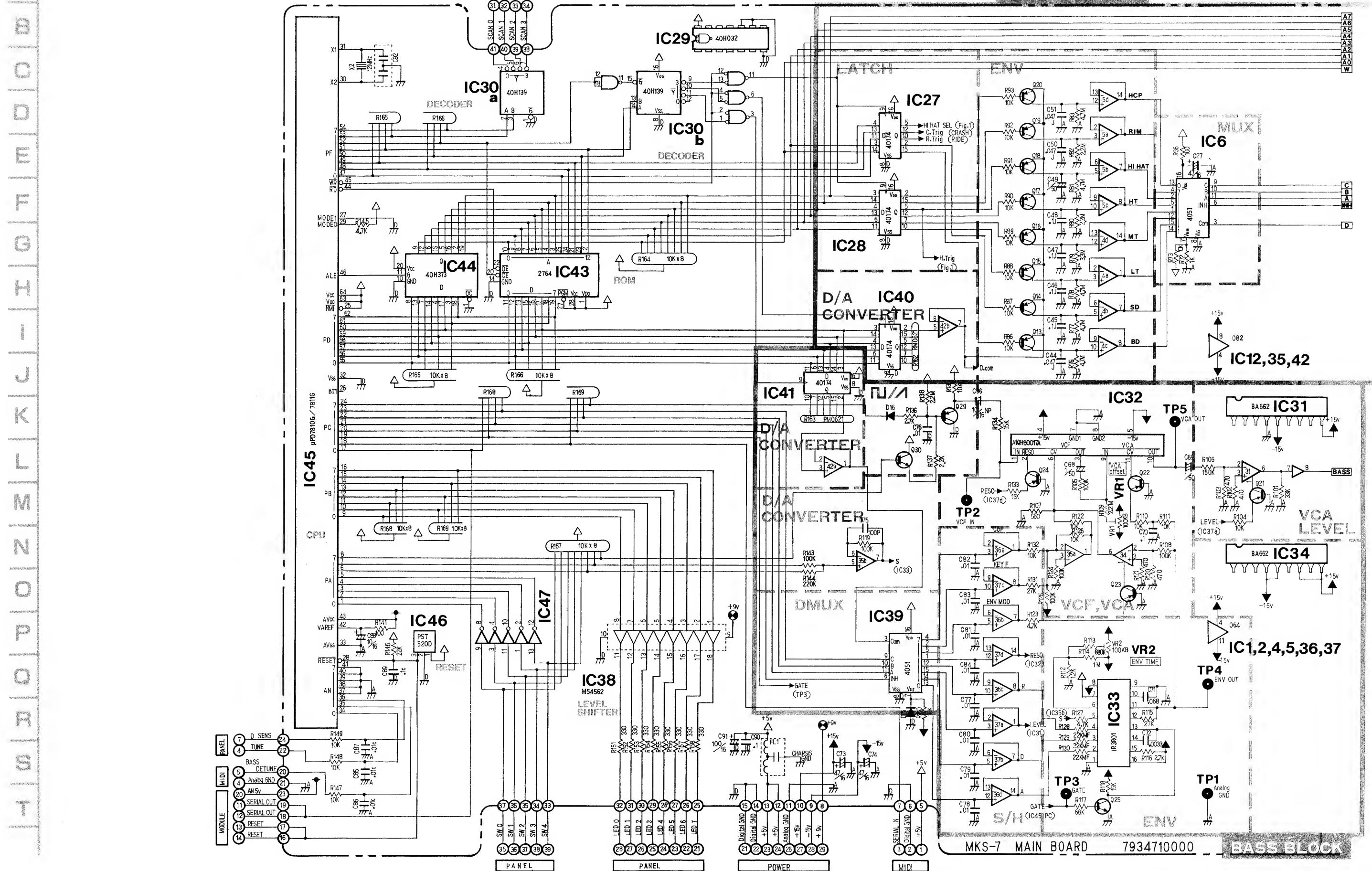
C11 DE7150472MVA1

D1~3 ISR 35-200

3 2A 125V (100V/117V)
 T1A 250V (220V/240V)
 1A 250V (100V/117V)
 T315mA 250V (220V/240V)

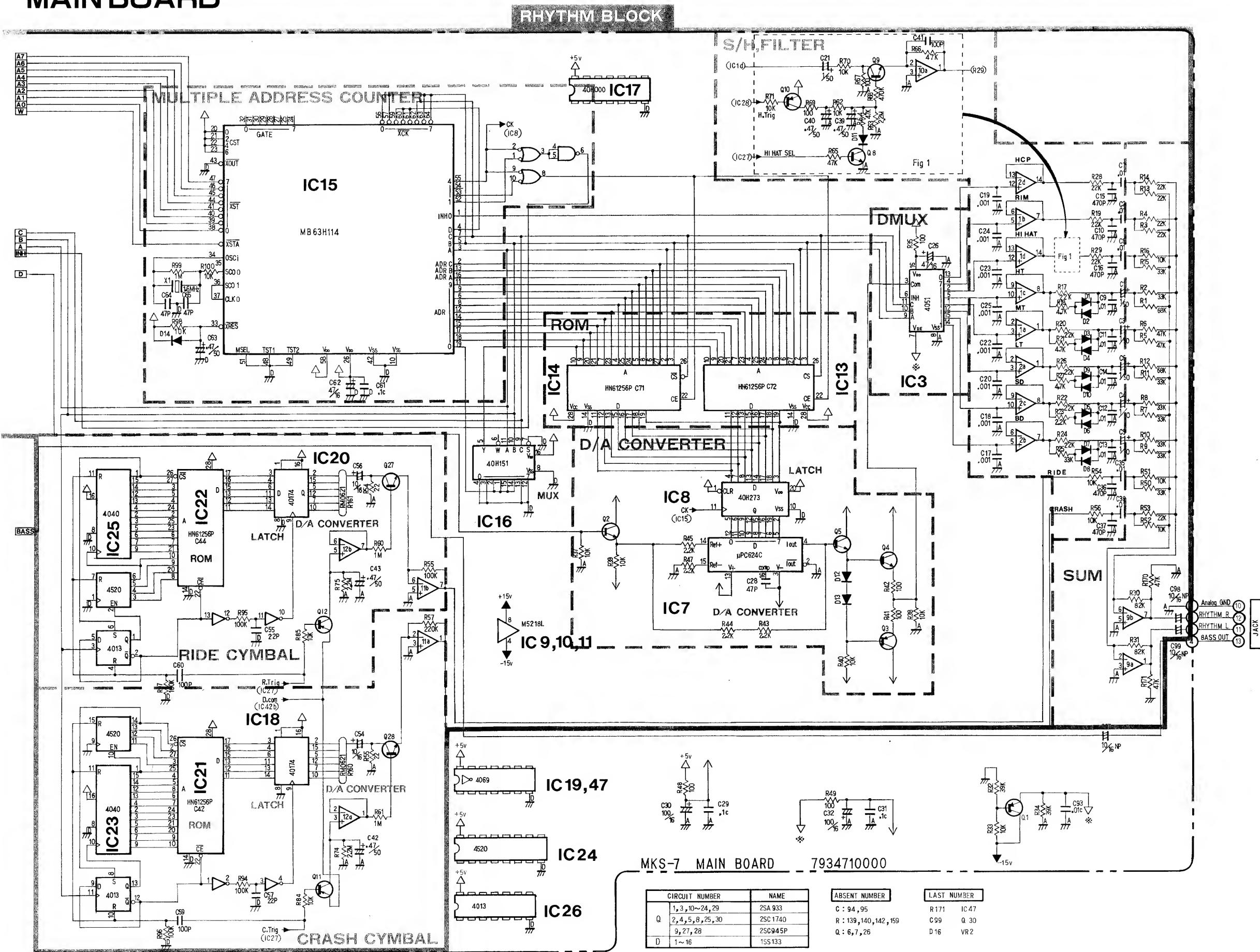
1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28

MAIN BOARD



1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28

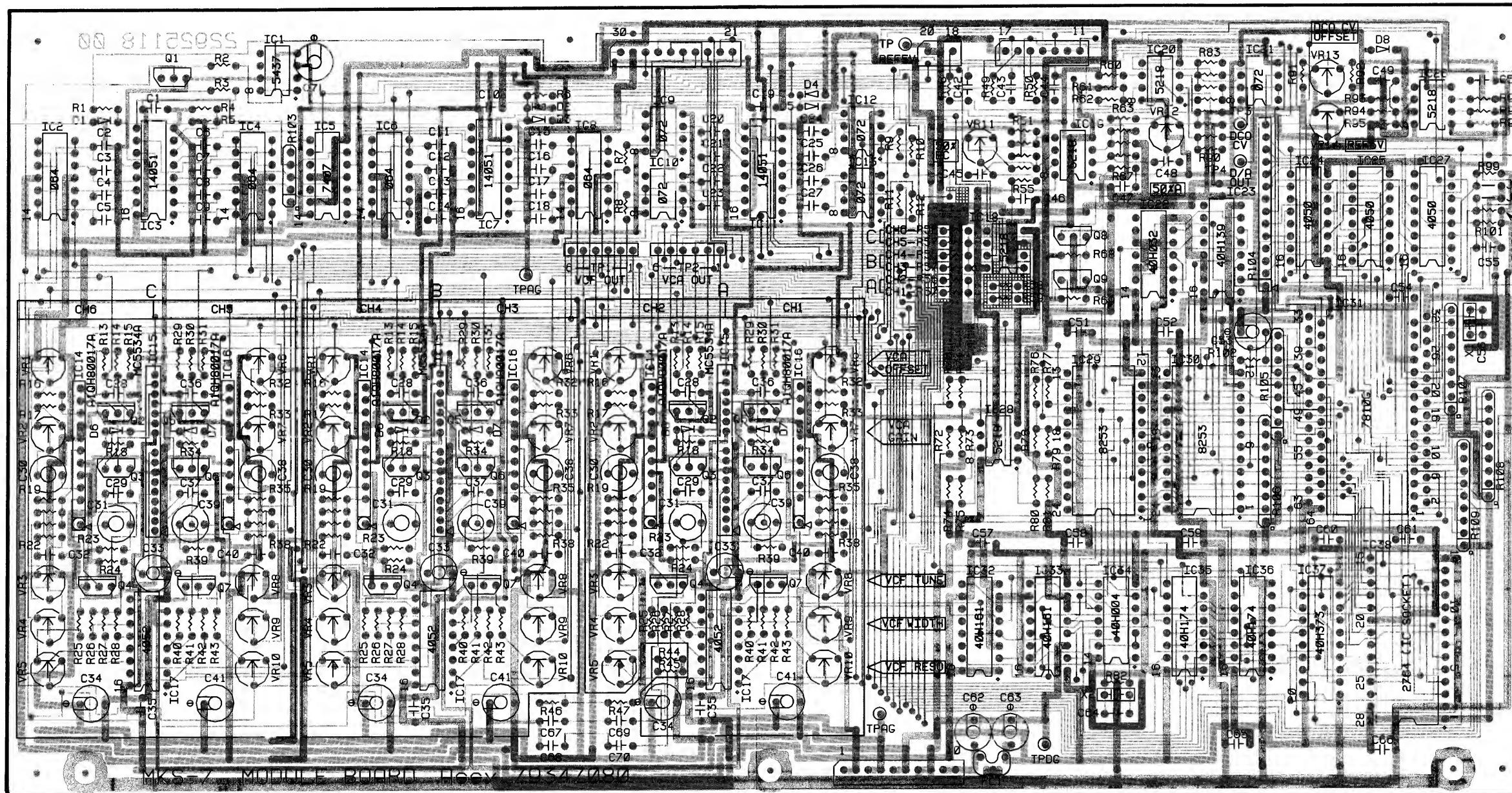
MAIN BOARD



A B C D E F G H I J K L M N O P Q R S T U V W X Y Z

1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 |

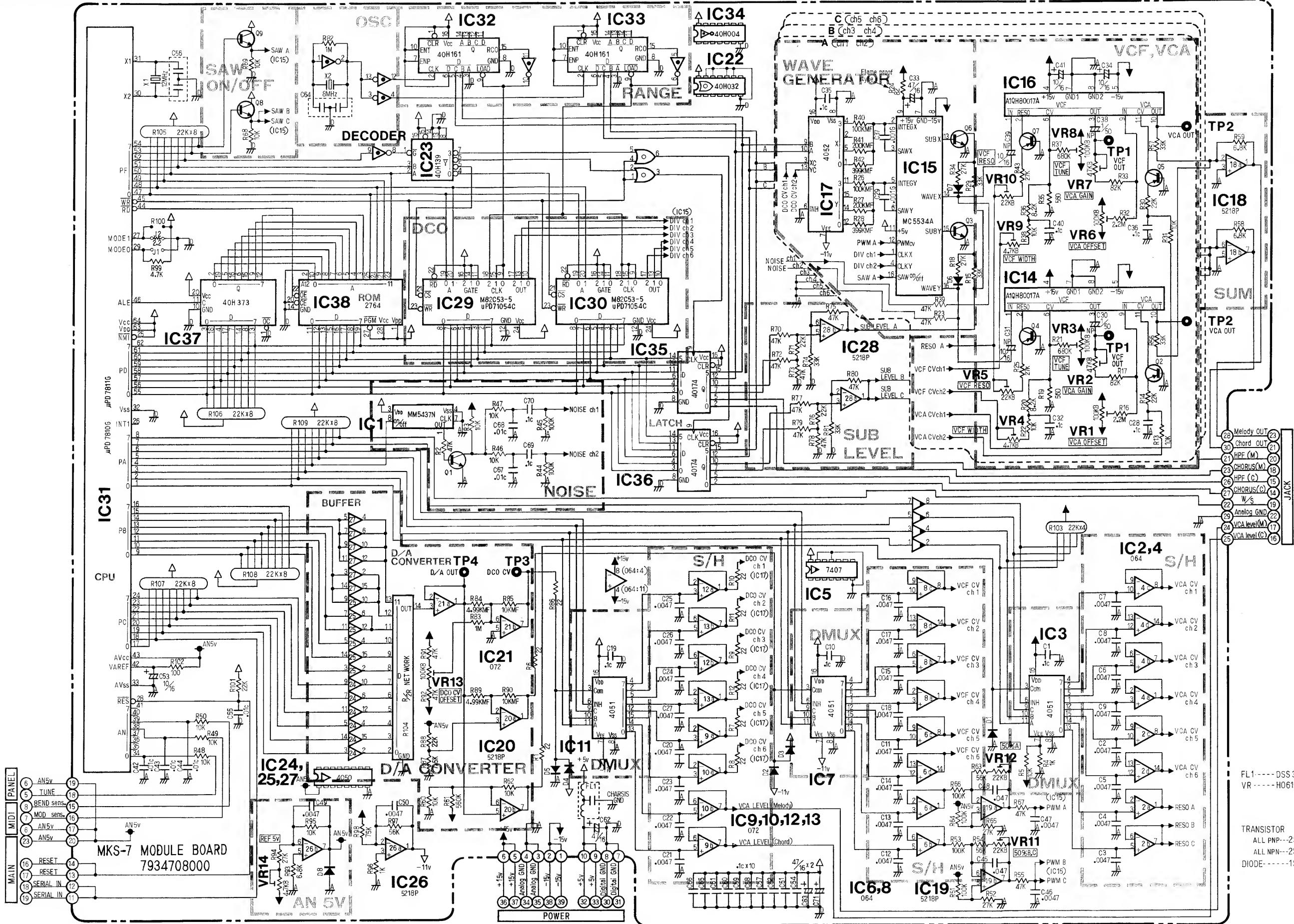
MODULE BOARD 7934708000 (pcb 22925118)



View from component side.

1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 |

MODULE BOARD

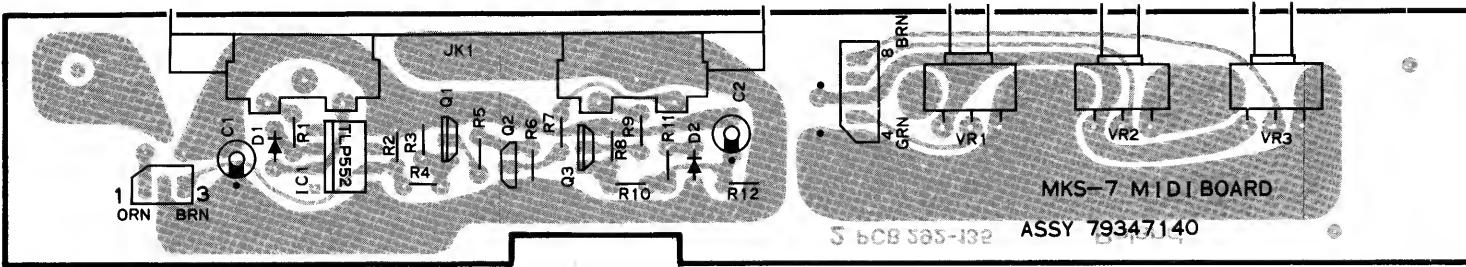


FL1-----DSS 310-55D223S
VR -----H0615C119-

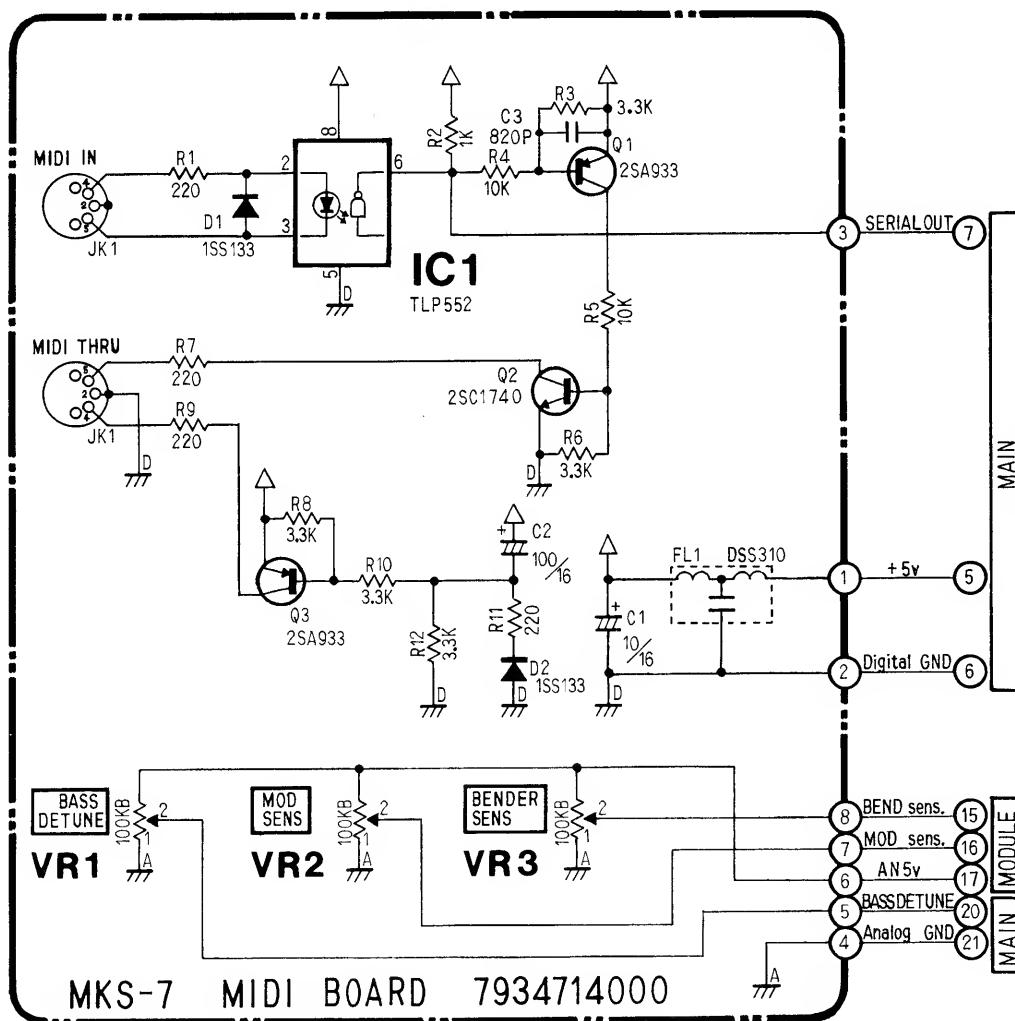
TRANSISTOR
ALL PNP---2SA 933
ALL NPN---2SC 1740
DIODE-----1SS 133

1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28

MIDI BOARD 7934714000 (pcb 22925135)

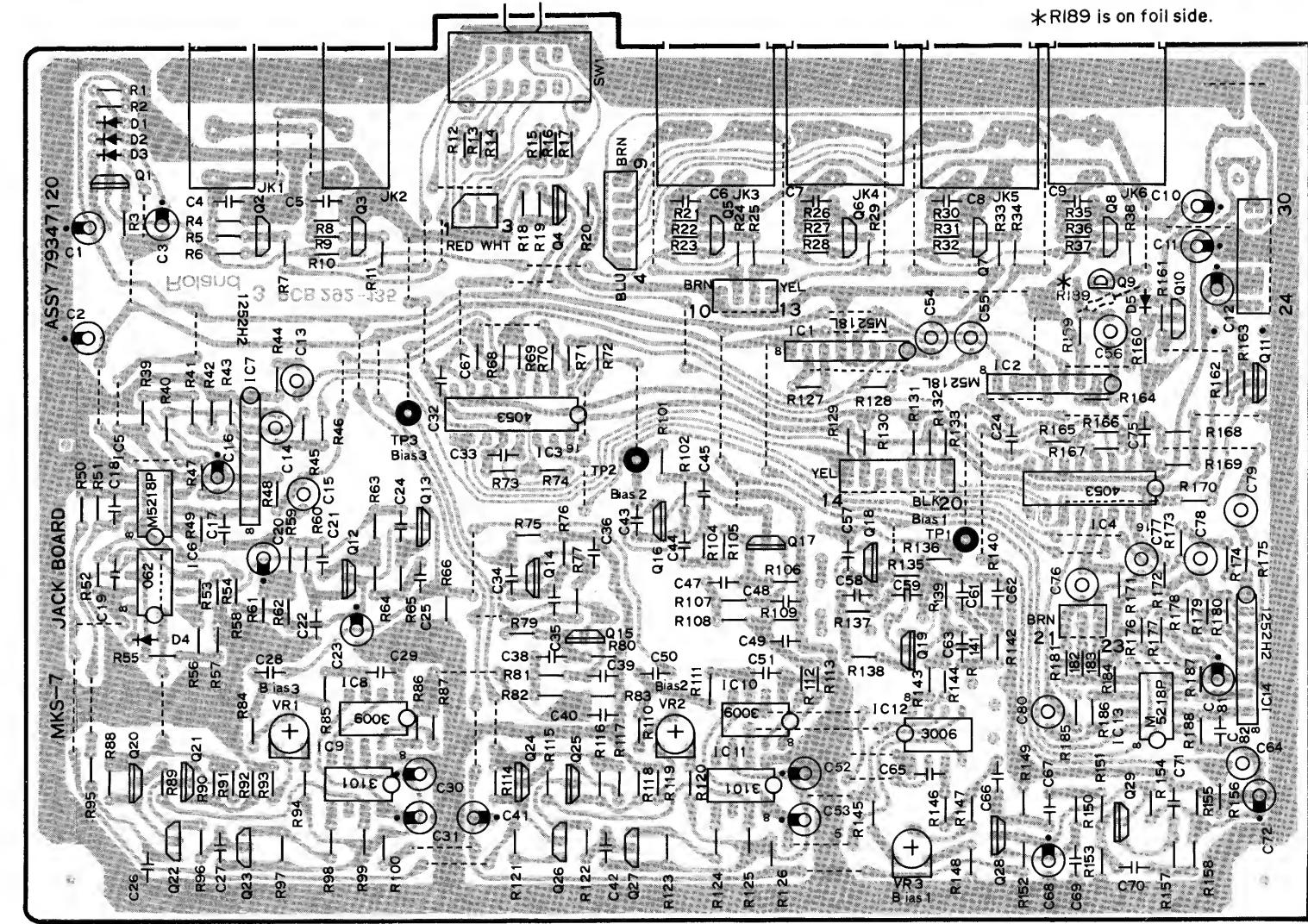


View from component side.



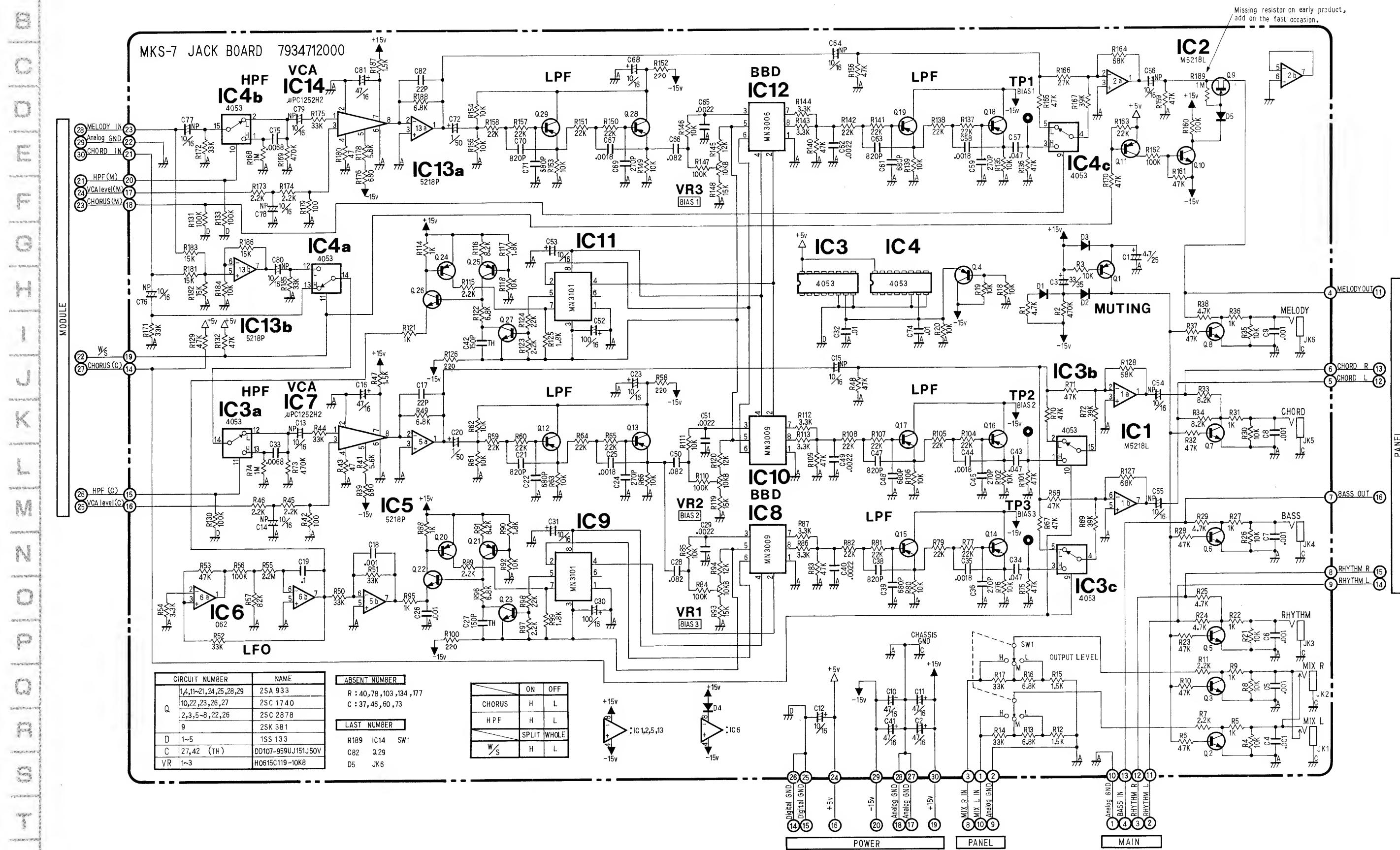
MKS-7 MIDI BOARD 7934714000

JACK BOARD 7934712000 (pcb 22925135)



View from component side.

JACK BOARD



Sound module

MODEL **MKS-7** MIDI Implementation Chart

Melody Block

Function.....	Transmitted	Recognized	Remarks
Basic Channel	Default Changed	×	1 1 – 16
Mode	Default Messages Altered	×	Mode 3 X *****
Note Number	True voice	×	0 – 127 *****
Velocity	Note ON Note OFF	○ X	X
After Touch	Key's Ch's	×	X
Pitch Bender	×	○	
Control Change	1 64	○ ○	Modulation Hold
Prog Change	True #	○ (0-127) ***** 0 – 99	
System Exclusive	×	○	Tone parameters
System Common	Song Pos Song Sel Tune	X X X	
System Real Time	Clock Commands	X X	X
Aux Mes-sages	Local ON/OFF All Notes OFF Active Sense Reset	X X ○ X	(123-127)
Notes		When the CHORD block is in 6 voice mode, the MELODY block cannot sound.	

Mode 1 : OMNI ON, POLY

Mode 3 : OMNI OFF, POLY

Mode 2 : OMNI ON, MONO

Mode 4 : OMNI OFF, MONO

○ : Yes

X : No

Chord Block

Function.....	Transmitted	Recognized	Remarks
Basic Channel	Default Changed	×	3 1 – 16
Mode	Default Messages Altered	×	Mode 3 X *****
Note Number	True voice	×	0 – 127 ***** 24 – 108
Velocity	Note ON Note OFF	○ X	X
After Touch	Key's Ch's	×	X
Pitch Bender	×	○	
Control Change	1 64 121	○ ○ ○	Modulation Hold Voice select (6/4 voices)
Prog Change	True #	○ (0-127) ***** 0-99	
System Exclusive	×	○	Tone parameters
System Common	Song Pos Song Sel Tune	X X X	
System Real Time	Clock Commands	X X	X
Aux Mes-sages	Local ON/OFF All Notes OFF Active Sense Reset	X X ○ X	(123-127)
Notes			

○ : Yes

X : No

Mode 1 : OMNI ON, POLY

Mode 3 : OMNI OFF, POLY

Mode 2 : OMNI ON, MONO

Mode 4 : OMNI OFF, MONO

Bass Block

Function.....	Transmitted	Recognized	Remarks
Basic Channel	Default Changed	×	2 1 - 16
Mode	Default Messages Altered	×	Mode 4 ×
Note Number	True voice	×	0 - 127 31 - 96
Velocity	Note ON Note OFF	○ ×	
After Touch	Key's Ch's	×	×
Pitch Bender	×	×	
Control Change			
Prog Change	True #	×	○ (0-127) 0-19
System Exclusive	×	○	Tone parameters
System Common	Song Pos Song Sel Tune	×	
System Real Time	Clock Commands	×	
Aux Mes-sages	Local ON/OFF All Notes OFF Active Sense Reset	×	○ (123-127)
Notes			

Mode 1 : OMNI ON, POLY
Mode 3 : OMNI OFF, POLY

Mode 2 : OMNI ON, MONO
Mode 4 : OMNI OFF, MONO

○ : Yes
× : No

Rhythm Block

Function.....	Transmitted	Recognized	Remarks
Basic Channel	Default Changed	×	10 1 - 16
Mode	Default Messages Altered	×	Mode 3 ×
Note Number	True voice	×	35 - 51 35 - 51
Velocity	Note ON Note OFF	○ ×	
After Touch	Key's Ch's	×	×
Pitch Bender	×	×	
Control Change			
Prog Change	True #	×	×
System Exclusive	×	×	
System Common	Song Pos Song Sel Tune	×	
System Real Time	Clock Commands	×	
Aux Mes-sages	Local ON/OFF All Notes OFF Active Sense Reset	×	
Notes			Note # to instruments assignment. 35,36 - Bass Dr 48,50 - Hi Tom 46 - Opn HH 38,40 - Snr Dr 37 - Rim Sht 49 - Crsh Cym 41,43 - Low Tom 39 - Hnd Clp 51 - Ride Cym 45,47 - Mid Tom 42,44 - Cls HH

Mode 1 : OMNI ON, POLY
Mode 3 : OMNI OFF, POLY

Mode 2 : OMNI ON, MONO
Mode 4 : OMNI OFF, MONO

○ : Yes
× : No

Mode 1 : OMNI ON, POLY
Mode 3 : OMNI OFF, POLY

Mode 2 : OMNI ON, MONO
Mode 4 : OMNI OFF, MONO

○ : Yes
× : No

Sound module

MODEL MKS-7 MIDI Implementation

1. RECOGNIZED RECEIVE DATA				1.3 BASS part				* When changing a tone parameter.				* Parameter number table															
1.1 MELODY part				Status Second Third Description				Byte Description				P # Function															
When the CHORD part is in 6 voice mode, the MELODY part is not available.																											
Status Second Third Description																											
1000 nnnn 0kkk kkkk 0vvv vvvv Note OFF, velocity ignored																											
1001 nnnn 0kkk kkkk 0000 0000 Note OFF, Roland ID #																											
kkkkkk = 0 - 127 (24 - 108) *1																											
1000 nnnn 0kkk kkkk 0vvv vvvv Note OFF, velocity ignored																											
1001 nnnn 0kkk kkkk 0000 0000 Note OFF, Operation code = Tone parameter change																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, velocity ignored																											
kkkkkk = 0 - 127 (24 - 108) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, Roland ID #																											
kkkkkk = 0 - 127 (24 - 108) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											
kkkkkk = 0 - 127 (19 - 96) *1																											
1001 nnnn 0kkk kkkk 0vvv vvvv Note ON, where nnnn + 1 = channel #																											